Revised Syllabus of

B.E. (EC/ECT/E&C/IE)

[Effective from -2014 – 2015]

Dr. Ulhas B Shinde
Chairman, Dr. B.A.M.U, Aurangabad
FINAL YEAR DEGREE COURSE IN ENGINEERING (REVISED)
(Applicable from the Academic Year 2014-2015)

1. All the Rules and Regulations, hereinafter specified shall be read as a whole for the purpose of interpretation.

ADMISSION

1. Admission to final year engineering shall be carried out as per the rules and regulations prescribed by the competent authority as appointed by the Government of Maharashtra and Dr. Babasaheb Ambedkar Marathwada University, Aurangabad, from time to time.

DURATION AND COURSES OF STUDY

1. The duration of the course is four years. Each of the four academic years shall be divided into two semesters herein after referred to as the semester I and semester II in chronological order. Each semester shall comprise

| Instructions | .................. 15 weeks |
| Preparation holiday | ............... 2 weeks or 15 days (Includes practical exams) |

2. Candidate who fails to fulfill all the requirements for the award of the degree as specified hereinafter within eight academic years from the time of admission, will forfeit his/her seat in the course and his/her admission will stand cancelled.

RULES AND REGULATION OF ATTENDANCE

1. Candidates admitted to a particular course of study are required to pursue a “Regular course of study” as prescribed by the University before they are permitted to appear for the University Examination.
2. “A regular course of study” means putting in attendance not less than 75% for individual subject.
3. a) In special cases and for sufficient causes shown, the Principal of the institute may, on the specific recommendation the Head of the Department, condone the deficiency in attendance to the extent of 15 % on medical ground subject to submission of medical certificate.

b) However, in respect of women candidates who seek condonation of attendance due to pregnancy, the Principal may condone the deficiency in attendance to the extent of 25 % (as against 15 % Condonation for other) on medical grounds subject to submission of medical certificate to this effect. Such condonation be availed twice during the entire course of study leading to degree in Engineering and Technology.
4. “Active Participation in N.C.C/N.S.S. Camps or Inter collegiate or Inter University or Inter State or International matches or debates of Educational Excursions or such other Inter University activities as approved by the authorities involving journeys outside the city in which the college is situated will not be counted as absence. However, such ‘absence shall not exceed (4) weeks per semester of the total period of instructions. Such leave should not be availed more than twice during the entire course of study.

5. The attendance shall be calculated on individual papers/subjects from the date of commencement of the semester.

6. In case of the candidates who fail to put in the required attendance in a course of study, he/she shall be detained in the same class and will not be recommended to appear for the University examination.

7. A candidate detained in semester I should take readmission in next academic year as a regular student and shall have to complete all the theory and practicals as a regular student.

8. In case a candidate is detained in semester II, he/she should take admission to Semester II of next academic year and complete all the theory and practicals as a regular student of semester II.

9. In case of change of syllabus the candidate even if detained in semester II should take readmission in next academic year for Semester I and II as a regular student and complete all the theory and practical’s as a regular student.

SCHEME OF INSTRUCTIONS AND EXAMINATION

1. Instructions about the curriculum in the various subjects in each semester of all the four years shall be provided by the University.

2. The details of instruction period, examination schedule, vacations etc. shall be notified by the Principal of the College as per the University academic calendar.

3. The medium of instruction and examination shall be English.

4. At the end of each semester, University examinations shall be held as prescribed in the respective schemes of examination.
5. The examinations prescribed may include written papers, practical and oral, tests, inspection of certified sessional work in Drawing and Laboratories and work done by students in each practical examination, along with other materials prepared or collected as part of Lab work/Project.

6. All the rules for examinations prescribed by the University from time to time shall be adhered to.

7. A candidate shall be deemed to have fully passed the Examination of a semester, if he/she secures not less than the minimum marks/grade as prescribed.

8. Institutions will be encouraged to adopt modern tools in classroom/labs to deliver the course contents.

9. Institutions will be encouraged to conduct online class tests.

O.874

The Final Year Examination in Engineering will be held in two parts B.E. semester-I and B.E. semester-II. No candidate will be admitted to B.E. semester-I examination unless he/she produce testimonials of having kept one term, for the subject under T.E. semester-I and II satisfactorily in a college of engineering affiliated to this University after passing the Third year examination of engineering other examination recognized as equivalent thereto as per the admission rules to Final year engineering prescribed by the Government of Maharashtra and Dr. B.A.M.University from time to time.

R.1861

i. In case a candidate fails in one or more heads of passing at the B.E. semester-I Examination after taking that examination at the end of first term as a regular student, he/she will be allowed to appear again for only those heads of passing in which he/she has failed at his/her immediately subsequent semester-I examination.

ii. That the marks obtained by the candidate at semester-I Examination shall be carried forward unless the candidate desires to appear for a paper in which he has failed and then gracing of marks should be done as a whole for semester-I and semester-II examination taken together.

R.1862

a) Candidates who secure 45% or more but less than 50% marks in the aggregate and pass the examination will be declared to have passed the examination in Pass Division.

b) Candidates who secure 50% or more but less than 60% marks in the aggregate and pass the examination will be declared to have passed the examination in Second Division.

c) Candidates who secure 60% or more but less than 66% marks in the aggregate and pass the examination will be declared to have passed the examination in first Division.

d) Candidates who secure 66% or more marks in the aggregate and pass the examination will be declared to
have passed the examination in First Division with Distinction.

e) For calculating the percentage for the purpose of giving weightage while awarding division in Final Examination to the students admitted to first year engineering, the maximum marks prescribed and the marks obtained by the examinee in the particular examinations shall be taken into consideration with the following weightages.

**F.E. - 10%, S.E. - 10%, T.E. – 40%, B. E. – 40%**

This shall be applicable for the students admitted in first year from academic year 2011-2012 onwards.

f) In case of the students directly admitted to the second year, the weightage while awarding Division in Final Examination the maximum marks prescribed and the marks obtained by the Examinee in the particular examinations shall be taken in to consideration

**S.E. - 20%, T.E. – 40%**

**B. E. – 40%**

This shall be applicable for the students admitted in second year from academic year 2012-2013 onwards.

**R.1863**

In case a candidate fails in the examination but desires to appear again thereat.

a) He may, at his option, claim exemption from appearing in the head or heads of passing in which he has passed.

b) Such exemption, if claimed, shall cover all the heads of passing in which it can be claimed.

c) Such exemption, if not availed of at the immediately subsequent appearance of the candidate at the examination, shall be deemed to have lapsed.

d) He /She may, at his option claim exemption from appearing in head or heads of passing of his choice and appear in the remaining head or head/s of passing to make-up the deficiency in the aggregate, if he has passed in all the heads of passing but has failed to secure a minimum of 45% of the aggregate marks.

e) The Marks obtained by a candidate for such term work as separately assessed will be carried over unless fresh term work is presented by him. A candidate whose marks are thus carried over shall be eligible for a division provided he/she does not avail himself of exemption in any head of passing excepting term work.

f) For the purpose of deciding whether a candidate claiming exemption in accordance with (a), (b), (c) above or (d) and (e) above has as required by R.260 secures 45% of the total marks obtainable in the whole examination the marks at his/ her previous examination/examination in the head or heads of passing in which he/she is exempted will be carried over. Candidates passing the examination in this manner shall not be eligible for a division or prizes or scholarships at the examination.
R.1864

RULE FOR COMBINED PASSING

1) To pass the examination a candidate must obtain minimum 40% of Marks in each Theory Paper & class test taken together however the candidate must obtain minimum 35% of Marks at the University theory Examination. The candidate must obtain a minimum aggregate of 45% of the total Marks obtainable at the T.E. Semester -I & II Examination taken together.

To pass a subject where there is no provision of class test, the candidate must obtain 40% of Marks in the University Examination.

Gracing should be done for the performance at University Examination or University Examination and class test taken together.

Minimum two class tests should be conducted in a semester for the theory subject if provided. The average performance of the Two-class tests should be forwarded to the University by the college along with the term work marks.

If candidate fails to secure 40% of marks at university theory examination and class test taken together at the regular semester examination, then he/she shall have to appear for university examination from subsequent examination onwards and secure 40% of marks at university examination and earlier obtained class test marks taken together. The improved performance at the university examination should not be considered for the Merit/Medal/Prize etc.

If the candidate remains absent for the class-test, his performance should be treated as ‘Zero’ Marks. Minimum marks required for passing in term work and practical shall be 40%. If a candidate secures less than 40% in any of the term work or fails to submit term work shall be detained in the same class.

R.1865

GENERAL RULES OF EXAMINATION

1. Application for permission to appear at every examination shall be made in the prescribed format accompanied by one passport size full face photograph (not profile) along with the necessary certificates and the prescribed fee, should be submitted to the Principal of the institute on or before the date fixed for this purpose.

2. When a candidate’s application is found in order and he/she is eligible to appear at an Examination,
the Principal of the institute is empowered to furnish him/her with a Hall-Ticket with the photograph affixed to it, enabling the candidate to appear in the Examination, and this Hall-Ticket shall have to be produced by the Candidate before he/she is admitted to the premises where the Examination is being held.

3. A Candidate who does not present himself/herself for the examination for any reason whatsoever, excepting shortage of attendance, shall not be entitled to claim refund of the whole or part of the examination fee, for subsequent Examination(s).

4. As engineering is a full time course, no candidate shall be allowed to put in attendance for a course or appear at examinations for different degrees and different faculties at one and the same time.

5. Students who have appeared once at any examination of the course need not put in fresh attendance, if they wish to reappear at the corresponding examination, notwithstanding the fact that the College may have introduced new subject. They will, however, have to appear at the examinations according to the scheme of examination and syllabi in force

R.1866
EQUIVALENCE OF THE SUBJECTS

Whenever a course or scheme of instruction is changed in a particular year, three more examinations immediately following thereafter shall be conducted according to the old syllabi/regulations. Also candidates not appearing at the examinations or failing in them shall take the examination subsequently according to the changed syllabi/ regulations as per the equivalence of the subjects as prescribed by the University.

Proposed Coding System of Subject/Paper

Six digit code for a subject (UG course)

<table>
<thead>
<tr>
<th>Batch</th>
<th>Year</th>
<th>Subject no</th>
</tr>
</thead>
<tbody>
<tr>
<td>CED</td>
<td>1. First Year UG</td>
<td>Semester-I</td>
</tr>
<tr>
<td>MED</td>
<td>2. Second Year UG</td>
<td>1-20 Theory</td>
</tr>
<tr>
<td>EEP</td>
<td>3. Third Year UG</td>
<td>21-30 practical</td>
</tr>
<tr>
<td>ECE</td>
<td>4. Fourth Year UG</td>
<td>31-40 Service Courses</td>
</tr>
<tr>
<td>EXE</td>
<td>5. Fifth Year UG</td>
<td>41-49 Electives</td>
</tr>
<tr>
<td>ETC</td>
<td></td>
<td>Semester-II</td>
</tr>
<tr>
<td>IEX</td>
<td></td>
<td>51-70 Theory</td>
</tr>
<tr>
<td>PED</td>
<td></td>
<td>71-80 Practical</td>
</tr>
<tr>
<td>CSE</td>
<td></td>
<td>81-90 Service Courses</td>
</tr>
<tr>
<td>CTD</td>
<td></td>
<td>91-99 Electives</td>
</tr>
<tr>
<td>COE</td>
<td></td>
<td></td>
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<td>ITD</td>
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<tr>
<td>EEE</td>
<td></td>
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<tr>
<td>ARH</td>
<td></td>
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<tr>
<td>BSH</td>
<td></td>
<td></td>
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<tr>
<td>BTD</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
### Structure of syllabus of subject

<table>
<thead>
<tr>
<th>Code No:</th>
<th>Title:</th>
</tr>
</thead>
</table>

#### Teaching Scheme

<table>
<thead>
<tr>
<th>Theory: hours/week</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tutorial: hours/week</td>
<td>Class Test: Marks</td>
</tr>
<tr>
<td>Practical/ TermWork : hours/week</td>
<td>Theory examination: Maximum hours</td>
</tr>
<tr>
<td>Practical/ Oral examination: Maximum Marks</td>
<td></td>
</tr>
</tbody>
</table>

#### Objectives:

1.
2.
3.

#### Unit 1:

#### Unit 2:

#### Unit 3:

#### Unit 4:

#### Unit 5:

#### Unit 6:

#### Text Books: 1

2

#### Reference Books: 1

2

3

4

#### Pattern of Question Paper:

The six units in the syllabus shall be divided in two equal parts i.e 3 units respectively. Question paper shall be set having two sections A and B. Section A questions shall be set on first part and Section B questions on second part. Question paper should cover the entire syllabus.

**For 80 marks Paper:**

1. Minimum ten questions
2. Five questions in each section
3. Question no. 1 from section A and Question no. 6 from section B, 10 marks each, will be compulsory.
4. Two questions from remaining questions from each section A and Band students are supposed to solve two questions from each section having weightage of 15 marks

**For 40 marks Paper:**

1. Minimum eight questions
2. Four questions in each section
3. Question no 1 from section A and Question no 5 from section B be made compulsory and should have at least five bits of two marks out of which three to be solved.
4. Two questions from remaining questions from each section be asked to solve having weightage of 7 marks.
0.95 GRACE MARKS FOR PASSING IN EACH HEAD OF PASSING (THEORY / PRACTICAL / ORAL / SESSIONAL) (EXTERNAL / INTERNAL)

The examinee shall be given the benefit of grace marks only for passing in each head of passing (Theory/practical/Oral/Sessional) in external or Internal examination as follows:-

<table>
<thead>
<tr>
<th>Head of passing</th>
<th>Grace Marks upto</th>
</tr>
</thead>
<tbody>
<tr>
<td>Up to 50</td>
<td>2</td>
</tr>
<tr>
<td>051 to 100</td>
<td>3</td>
</tr>
<tr>
<td>101 to 150</td>
<td>4</td>
</tr>
<tr>
<td>151 to 200</td>
<td>5</td>
</tr>
<tr>
<td>201 to 250</td>
<td>6</td>
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<tr>
<td>251 to 300</td>
<td>7</td>
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<tr>
<td>301 to 350</td>
<td>8</td>
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<td>351 to 400</td>
<td>9</td>
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<tr>
<td>And 401 and above</td>
<td>10</td>
</tr>
</tbody>
</table>

Provided that the benefit of such gracing marks given in different heads of passing shall not exceed 01 (one) percent of the aggregate marks in that examination.

Provided, further that the benefit of gracing of marks under this ordinance shall be applicable only if the candidate passes the entire examination of semester/year.

Provided further that this gracing is concurrent with the rules and guidelines of professional statutory bodies at the All India level such as AICTE, MCI, Bar Council, CCIM, CCIH, NCTE, UGC etc.

0.96 GRACE MARKS FOR GETTING HIGHER CLASS

A candidate who passes in all the subjects and heads of passing in the examination without the benefit of either gracing is condonation rules and whose total number of marks falls short for securing Second Class/Higher Second class of First Class by marks not more than 01 percent of the aggregate marks of that examination or up to 10 marks, whichever is less, shall be given the required marks to get the next higher class or grade as the case may be.

Provided that benefit of the above mentioned grace marks shall not be given, if the candidate fails to secure necessary passing marks in the aggregate head of passing also, if prescribed in the examination concerned.

Provided further that this gracing is concurrent with the rules and guidelines of professional statutory bodies at the All India level such as AICTE, MCI, Bar Council, CCIM, CCIH, NCTE etc.

0.97 GRACE MARKS FOR GETTING DISTINCTION IN THE SUBJECT ONLY.

A candidate who passes in all the subject/heads of passing in the examination without benefit of either gracing or condonation rules and whose total number of marks in the subject/s falls short by not more than three marks for getting distinction in the subject/s shall be given necessary grace marks up to three in maximum two subjects, subject to maximum 01(one) percent of the total marks of that head of passing whichever is more, in a given
examination.
Provided that benefit of the above mentioned grace marks shall be given to the candidate only for such examination/s of which provision for distinction in a subject has been prescribed.
Provided further that this gracing is concurrent with the rules and guidelines of professional statutory bodies at the All India level such as AICTE, MCI, Bar council, CCIM, CCIH, NCTE etc.

0.98 CONDONATION

If a candidate fails in only one head of passing, having passed in all other heads of passing, his/her deficiency of marks in such head of passing may be condoned by not more than 01 percent of the aggregate marks of the examination or 10 percent of the total number of marks of the head of passing in which he/she is failing, whichever is less. However, condonation, whether in one head of passing or aggregate head of passing be restricted to maximum upto 10 marks only.
Condensation of deficiency of marks be shown in the statement of marks in the form of asterisk and ordinance number.
Provided that this condonation of marks is concurrent with the rules and guidelines of Professional statutory bodies at the all india level such as AICTE, MCI, Bar council, CCIM, CCIH, NCTE etc.

0.106 (A) UNFAIR MEANS COMMITTED BY THE STUDENT

1. The Board of Examinations shall be the competent authority to take disciplinary action against a student for his misconduct due to his unfair means committed by him at the examination conducted by the University.

2. The Principal, of the college or Head of the recognized Institution shall be the competent authority to take disciplinary action against a student for his misconduct due to his unfair means committed by him at the examination conducted by the University, recognized Institution of behalf of the University.

3. Definition- Unless the context otherwise requires

(a) Student means and includes a person who is enrolled as such by the University/college/Institution for receiving instruction qualifying for any degree, diploma or certificate awarded by the University. It includes ex-student and student registered as candidate (examinee) for any of the Degree, Diploma or Certificate examinations.

(b) Unfair Means includes one or more of the following acts or omissions on the part of student/s during the examination period.

i. Possessing unfair means material and or copying there from.

ii. Transcribing any unauthorized material or any other use thereof.
iii. Intimidating or using obscene language or threatening or use of violence against invigilator or person
on duty for the conduct of examination or man-handling him/her or leaving the examination hall without
permission of the supervisor or causing disturbances in any manner in the examination proceedings.
iv. Unauthorized communicating with other examinees or any one else inside or out side the examination hall.
v. Mutual/Mass copying
vi. Smuggling out, either blank or written or smuggling in of answer books as copying material.
vi. Smuggling in blank or written answer book, forging and forging signature of the Jr.
Supervisor therein.
viii. Interfering with or counterfeiting of University/College Institution seal or answer books or office stationary
used in the examination.
ix. Impersonation at the University/college/Institution examination.
x. Revealing identity in any form in the answer written or in any other part of the answer book by the student at
the University or College or Institution examination.
x. Or any other similar act/s omission/s which may be considered as unfair means by the competent authority.

(c) “Unfair means relating to examination” means and includes directly or indirectly communicating
or attempting to commit or threatening to commit any act or coercion, undue influence or fraud or malpractice
with a view to obtaining wrongful gain to him or to any other person or causing wrongful loss to other person/s.

(d) “Unfair means material” means and includes any material whatsoever, related to the subject of the examination,
printed, typed, handwritten or otherwise on the person or on clothes, or body of the student (examinee) or on wood
or other material, in any manner or in the form of chart, diagram, map or drawing or electronic aid etc. which is not allowed in the examination hall.

(e) “Possession of unfair means material by a student” means having any unauthorized material on his/her
person or desk or chair or table or at any place within his/ her reach, in the examination centre and its environs or
premises at any time from the commencement of the examination till its conclusion.

(f) “Student found in possession” means a student reported in writing as having been found in possession of
unfair means material by Jr. Supervisor, Sr. Supervisor, member of the Vigilance committee or
Examination squad or any other person authorized for this purpose in this behalf, even if the unfair means material
is not produced as evidence because of its being reported as swallowed or destroyed or snatched away or
otherwise taken away or spoiled by the student or by any other person acting on his behalf to such an extent
that it has become illegible.

Provided that report to that effect is submitted by the Sr. Supervisor or chief Conductor or any other authorized
person to the Controller of Examinations, Principal or Head of the Institutions concerned or any officer
authorized in this behalf.

(g) Material related to the subject of Examination means and includes, if the material is produced as evidence any
material certified as related to the subject of examination by a competent person and if the material is not produced as evidence or has become illegible for any of the reasons referred to in clause (f) above, the presumption shall be that the material did relate to the subject of the examination.

(h) “Chief Conductor”, means and includes, Principal of the College concerned, or Head of the recognized institution concerned where concerned examination is being conducted and any other person duly authorized by him or person appointed as In charge of examination, by the authority competent to make appointment to such post.

4. Where the examination of the University courses are conducted by the constituent college/recognized Institute on behalf of the University, the Principal/Head of the concerned college/recognized Institution on receipt of a report regarding use of unfair means by any student at any such examination including breach of the rules laid down by the Management council or by the College/recognized institution for proper conduct of examination, shall have power at any time to institute inquiry and to punish such unfair means or breach of any of the rules by exclusion of such a student from any such examination or any University course in any college/Institution either permanently or for a specified period or by cancellation of the result of the student in the college/recognized Institution examination for which he/she appeared or by deprivation of any college/Institution scholarship or by cancellation of the award of any college/Institution prize or medal to him/her or by imposition of fine not exceeding Rs.300/- or in any two or more of the aforesaid ways.

5. During examination, examinees and other students shall be under disciplinary control of the Chief Conductors.

6. Chief Conductor/s of the examination centre shall in the case of unfair means, follow the procedure as under:-

(a) The student shall be called upon to surrender to the Chief Conductor, the unfair means material found in his or her possession, if any, and his/her answer-book.

(b) Signature of the concerned student shall be obtained on the relevant materials and list thereon. Concerned Senior Supervisor and the Chief Conductor shall also sign on all the relevant materials and documents.

(c) Statement of the student and his undertaking in the prescribed format and the statement of the concerned Jr. Supervisor and Sr. Supervisor shall be recorded in writing by the Chief Conductor (Appendix-III). If the student refuses to make statement or to give undertaking the concerned Sr. Supervisor and / or Chief Conductor shall record accordingly under their signature.

(d) Chief Conductor shall take one or more of the following decisions depending upon seriousness/gravity of the case:-

i) In the case of impersonation or violence, expel the concerned student from the examination and not allow him/her to appear for remaining examination.

ii) Obtain undertaking from the student to the effect that the decision of the concerned competent authority in his/her case shall be final and binding and allow him/ her to continue with his/ her examination.
iii) May report the case to the concerned Police Station as per the provision of Maharashtra Act No. XXXI 1982 – An act to provide for preventing Malpractice’s at University Board and other specified examinations (Appendix-III) (Performa A & B).
iv) Confiscate his / her answer books, mark it as suspected unfair means case and issue him/her fresh answer books duly marked.
v) All the material and list of material mentioned in sub-clause (a) and the undertaking with the statement of the student and that of the Jr. Supervisor as mentioned in clause no. (b) & (c) and the answer-book/s shall be forwarded by the Chief conductor along with his report to the concerned Controller of Examinations/Principal/Head of the Institution, as the case may be, in a separate and confidential sealed envelope marked “suspected unfair means case”
vi) In case of unfair means of oral type, the Jr. Supervisor and the Sr. Supervisor or concerned authorized person shall record the facts in writing and shall report the same to the concerned Controller of Examinations/Principal/Head of the Institutions, as the case may be.

PUNISHMENT

The competent authority concerned i.e. the Board of Examinations in the case of University examination, the concerned Principal in the case of college examinations held by the recognized Institutions, after taking into consideration the report of the committee shall pass such orders as it deem fit including granting the student benefit of doubt, issuing warning or exonerating him/her from the charges and shall impose any one or more of the following punishment on the student/s found guilty of using unfair means:-

(a) Annulment of performance of the student in full or in part in the examination he/she has appeared for.

(b) Debarring student from appearing for any examination of the University or college Institution for a stipulated period not exceeding five year.

(c) Debarring student from appearing for any examination of the University or college Institution for a stipulated period not exceeding five year.

(d) Cancellation of the University or College or Institution scholarship/s or award/s prize or medal etc. awarded to him/her in that examination.

(e) In addition to the above mentioned punishment, the competent authority may impose a fine not exceeding Rs.300/- on the student declared guilty. If the student concerned fails to pay the fine within a stipulated period, the competent authority may impose on such a student additional punishment/penalty as it may deem fit.

(f) The student concerned be informed of the punishment finally imposed on him/her in writing by the competent authority or by the officer authorized by it in this behalf, under intimation to the College/Institution
he/she belongs to.

(g) An appeal against the findings of the committee shall lie with the concerned competent authority whose decision shall be final and binding.

(h) An appeal made in writing within a period of 30 days from the date imposition of the punishment shall be considered by the competent authority on merit and shall be decided on the basis of the evidence available in the case and shall be heard in person in deserving cases, if the competent authority finds substance in the appeal, the competent authority shall supply a typed copy of the relevant extract of fact-finding report of the inquiry committee, as well as documents relied upon (if not strictly confidential). Decision in the appeal shall be informed to the student concerned accordingly.

(i) The court matters in respect of the unfair means cases should be dealt with by the respective competent authority.

(j) As far as possible the quantum of punishment should be as prescribed (Category-wise in Appendix-I

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**APPENDIX-I**

THE BROAD CATEGORIES OF UNFAIR MEANS ADOPTED BY STUDENTS AT THE UNIVERSITY/ COLLEGE/ INSTITUTION EXAMINATION AND THE QUANTUM OF PUNISHMENT FOR EACH CATEGORY THEREOF.

<table>
<thead>
<tr>
<th>Sr. No.</th>
<th>Nature of Malpractices</th>
<th>Quantum of Punishment</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Possession of copying material</td>
<td>(Note:- This quantum of punishment Shall apply also ot the following categories of malpractices at Sr. No. 2, to Sr. No.12 in addition to the Punishment prescribed thereat)</td>
</tr>
<tr>
<td>2.</td>
<td>Actual copying from the copying material</td>
<td>Exclusion of the student from university or College or Institution examination for one additional examination.</td>
</tr>
<tr>
<td>3.</td>
<td>Possession of another students Answer Book</td>
<td>Exclusion of the student from University or College or Institution examination for one additional examination (Both the students)</td>
</tr>
<tr>
<td>4.</td>
<td>Possession of another students Answer book+ actual evidence of Copying</td>
<td>Exclusion of the student from University or College or Institution examination for two additional examination (Both the Students)</td>
</tr>
<tr>
<td>5.</td>
<td>Mutual / Mass copying.</td>
<td>Exclusion of the student from University or College or Institution examination for two additional examinations.</td>
</tr>
<tr>
<td></td>
<td></td>
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</tr>
<tr>
<td>6 (a)</td>
<td>Smuggling out or smuggling in of Answer book as copying material.</td>
<td>Exclusion of the student from University or College or Institution examination for two additional examinations.</td>
</tr>
<tr>
<td>(b)</td>
<td>Smuggling in of written answer book based on the question paper set at the examination</td>
<td>Exclusion of the student from University or College or Institution examination for three additional examinations.</td>
</tr>
<tr>
<td>(c)</td>
<td>Smuggling in of written answer book and forging signature of Jt, Supervisor thereon</td>
<td>Exclusion of the student from University or College or Institution Examination for four additional examinations.</td>
</tr>
<tr>
<td>7.</td>
<td>Attempt to forge the signature of the Jr. Supervisor on the answer book or Supplement.</td>
<td>Exclusion of the student from the University or College or Institution examination for four additional examinations.</td>
</tr>
<tr>
<td>8.</td>
<td>Interfering with or counterfeiting of University / College/ Institution seal or Answer books or office stationary used in the examination</td>
<td>Exclusion of the student from University or College or Institution examination for four additional examinations.</td>
</tr>
<tr>
<td>9.</td>
<td>Answer book main or supplement written outside the examination hall or any other insertion in answer book.</td>
<td>Exclusion of the student from University or College or Institution examination for four additional examinations.</td>
</tr>
<tr>
<td>10.</td>
<td>Insertion of currency notes/to bribe or attempting to bribe any of the persons/s connected with the conduct of Examination</td>
<td>Exclusion of the student from University or College or Institution Examination for four additional examinations. (Note:- This money shall be created to the Vice-Chancellor’s Fund)</td>
</tr>
<tr>
<td>11.</td>
<td>Using obscene language/violence/ threat at the examination centre by a student at the University/ College / Institution Examination to Jr./ Sr. Supervisor/ Chief Conductor or Examiners.</td>
<td>Exclusion of the student from University or College or Institution examination for four additional Examinations.</td>
</tr>
<tr>
<td>12.(a)</td>
<td>Impersonation at the University/ College / Institution examination</td>
<td>Exclusion of the Student from University or College or Institution examination for five additional examinations, (Both the students if impersonator is University or College or Institute student)</td>
</tr>
<tr>
<td>Section</td>
<td>Description</td>
<td>Punishment</td>
</tr>
<tr>
<td>---------</td>
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</tr>
<tr>
<td>(b)</td>
<td>Impersonation by a University/ College/ Institute student at S.S.C./ H.S.C./ any other Examinations.</td>
<td>Exclusion of the Student from University or College or Institution examination for five additional examinations</td>
</tr>
<tr>
<td>13.</td>
<td>Revealing identity in any form in the answer written or in any other part of the Answer book by the student at the University or College or Institution Examination</td>
<td>Annulment of the performance of the student at the University or College or Institution Examination in full.</td>
</tr>
<tr>
<td>14.</td>
<td>Student found having written on palms or on the Body, or on the clothes while in the Examination</td>
<td>Annulment of the performance of the student at University or College or Institution Examination in full.</td>
</tr>
<tr>
<td>15.</td>
<td>All other mal-practices not covered in the aforesaid categories.</td>
<td>Annulment of the performance of the student at the University or college or Institution Examination in full and severe punishment depending upon the gravity or the offence.</td>
</tr>
<tr>
<td>16.</td>
<td>If on previous occasion a disciplinary action was taken against a student for malpractice used at examination and he/she is caught `again for malpractices used at the examinations, in this event he/she shall be dealt with severely. Enhanced punishment can be imposed on such student. This enhanced punishment may extend to double the punishment provided for the offence when committed at the second or subsequent examination.</td>
<td></td>
</tr>
<tr>
<td>17. <strong>PRACTICAL/DISSERTATION/PROJECT REPORT EXAMS.</strong></td>
<td>Student involved in malpractices at practical/ dissertation/ project report examination shall be dealt with as per the punishment provided for the theory examination.</td>
<td></td>
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<tr>
<td>18.</td>
<td>The competent authority in addition to the above mentioned punishments may impose a fine not exceeding Rs. 300/- on the student declared guilty.</td>
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<tr>
<td></td>
<td>Note:- The term annulment of performance in full’ includes performance of the student of the theory as well as annual practical examination, but does not include performance at term work, project work and dissertation examination unless malpractice used thereat.</td>
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<tr>
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<td>Digital Image Processing</td>
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<td>EXD402</td>
<td>Embedded Systems</td>
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<td>3</td>
<td>EXD403</td>
<td>VLSI Design</td>
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<td>4</td>
<td>EXD404</td>
<td>Microwave &amp; Radar Engineering (ECT/E &amp; C)</td>
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<tr>
<td>5</td>
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<td>Robotics (EC)</td>
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<td>6</td>
<td>EXD441-445</td>
<td>EL-1</td>
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<td>EXD421</td>
<td>Lab-1- Digital Image Processing</td>
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<tr>
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<td>EXD422</td>
<td>Lab-2- Embedded Systems</td>
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<td>EXD423</td>
<td>Lab-3- VLSI Design</td>
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<td>EXD424</td>
<td>Lab-4-Microwave &amp; Radar Engineering (ECT/E &amp; C)</td>
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<td>EXD425</td>
<td>Lab-5-Robotics (EC)</td>
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<td>EXD426</td>
<td>Lab-6-EL-I</td>
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<td>EXD427</td>
<td>Lab-7- Project -I</td>
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<td>Total of Part-I</td>
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</table>

**Elective –I**

**ECT**

EXD 441 - Artificial Neural Network & Fuzzy Logic
EXD 442 - Wireless Mobile Communication
EXD 443 - Biomedical Electronics
EXD 444 - Advanced Industrial Automation
EXD 445 - Open Elective-I

**Electronics & Communication**

EXD 441 - Advanced Power Electronics
EXD 442 - Consumer Electronics
EXD 443 - Microwave and Radar Engineering
EXD 444 - Advanced Industrial Automation
EXD 445 - Open Elective-I

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Chairman, Dr. BAMU, Aurangabad

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<table>
<thead>
<tr>
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<th>Sub Code</th>
<th>Subject</th>
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<th>CT</th>
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<td>Lab 2 - Optical Fiber Communication</td>
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<td>EXD476</td>
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<td>16</td>
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<td>14</td>
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<td>250</td>
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<td>Total of Part-I &amp; II</td>
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<td>26</td>
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<td>720</td>
<td>150</td>
<td>450</td>
<td>1500</td>
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</tr>
</tbody>
</table>

**Note:**
1. Minimum two tests should be conducted for each theory subject and average of best two tests should be considered.
2. If feasible, all the students shall undergo In-plant Training of two to four weeks in concerned Industry, during summer vacation. They should submit a report and give presentation on the same during Final Year.

L: Lecture Hours per week   T: Tutorial Hours per week   P: Practical Hours per week   CT: Class Test
TH: University Theory Examination   TW: Term Work   P: Practical /Oral Examination

**Elective –II**

**ECT**

- EXD 491- Antenna Theory & Wave Propogation
- EXD 492- ADSP
- EXD 493 -Robotics
- EXD 494 -Satellite Communication
- EXD 495 -Open Elective-II

**EC/IE**

- EXD491 -Biomedical Electr
- EXD 492-Mobile Com
- EXD 493- Satellite Commun
- EXD 494 -Industrial Drives
- EXD 495 -Open Elective-II

**Electronics & Communication**

- EXD 491 -Antenna
- Theory & Wave
- EXD 492 -ADSP
- EXD 493- Robotics
- EXD 494 -Satellite Communication
- EXD 495 -Open Elective-II

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# EXD-401 – Digital Image Processing

<table>
<thead>
<tr>
<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td></td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/Oral : 50 Marks</td>
</tr>
<tr>
<td></td>
<td>Term Work: -----</td>
</tr>
</tbody>
</table>

**Objective:**
1. This course gives the knowledge of fundamentals of Digital Image Processing.
2. Students will learn proper image representation, enhancement, filtering, restoration, analysis, reconstruction.
3. Students will learn advanced digital image processing techniques, and various image transformations, image reconstruction from incomplete information, image segmentation and recognition.

## Unit-1
### Introduction to Digital Image Processing:
- Digital Image, Digital image from analog image, Digital image representation, fundamental steps in image processing, elements of digital image processing systems, hardware for image processing system, image digitizer, Types of digitizer, Image digitizing components, Image Acquisition. Acquisition component, classification of image, Types of image, Different file format used.
- Digital image fundamentals: Elements of visual perception, a simple image model Sampling and quantization some basic relationship between pixels, image Geometry, Basic transformations, perspective transformation camera model and Calibration, stereo imaging.

## Unit-2
### Image Transform:
- 2-D Fourier transform, Fast Fourier transform, Properties, Other separable transforms, Walsh Transform, Hadamard Transform, Slant Transform, Discrete Cosine Transform, wavelet Transform, Haar function, Singular Value Decomposition, Application of SVD, with simple programs, numerical based on transformation.

## Unit-3
### Image Enhancement:
- Image enhancement in spatial domain, enhancement through point processing, Basic grey level Transformations, Histogram Processing, Enhancement using arithmetic and logic operations. Enhancement by point Processing Spatial Domain Filtering-smoothing and sharpening filters Frequency Domain Filtering- smoothing and sharpening filters with simple programs, numerical based on above.

## Unit-4
### Image Segmentation:
- Detection of discontinuities, Edge linking and boundary detection, Thresholding, Region based segmentation, Use of watershed, Image representation- Chain codes, Boundary and Regional Descriptors. with simple programs, numerical based on above

## Unit-5
### Image Compression:
- Need for image compression, Redundancies, classification of redundancies ,Fidelity criteria. Error free compression, image compression models, classification of image compression scheme ,elements of information theory, error free compression variable
length coding, Huffman coding, Arithmetic coding, bit plane coding, lossless predictive coding, lossy compression, predictive coding, transform coding, video compression, image compression standards- JPEG, MPEG.

### Unit-6

**Morphological Image Processing & Ip Applications:**

Basic operations dilation and erosion, opening and closing operations. Basic processing such as region filling, thinning, thickening, pruning, skeletons, convex hull for binary and grey scale images with simple programs, numerical based on above.

Applications: Biometric, Security, Communication, Medical imaging such as MRI, CT, X-ray, morphological in grey and binary images.

### Text Books/Reference Books:

6. Digital Image Processing PIKS Scientific Inside, 4ed, w/cd

### Practical Examination:

The students should do the MATLAB programming based on syllabus at least ten programmes in practical writeup.

**EXD-421 List of Experiments:**

1. Write a program to extract different attributes of an image
2. Write a program for Image negation, power Law correction
3. Write a program for Histogram mapping & equalization, stretching
4. Write a program for Image smoothing, sharpening
5. Write a program for Edge detection - use of Sobel, Prewitt and Roberts operators
6. Write a program for Morphological operations on binary images
7. Write a program for Morphological operations on Gray scale images
8. Write a program for Pseudo coloring
9. Write a program for Chain coding
10. Write a program for Image statistics
11. Write a program for DCT/IDCT computation
12. Write a program for Transform application assignment.

### PATTERN OF QUESTION PAPER:

Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks Papers:**

1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
### Objective:
1. To get students familiar with the typical problems and constraints that arise while designing and developing embedded systems.
2. To make students capable to design and implement an embedded system, following the model-centric design.
3. To introduce theoretical and practical solutions to typical problems that the students are expected to master.

#### Unit-1
**Embedded system Introduction:**
Introduction to Embedded System, Definition, overview, Design challenges, optimizing design metrics, common design metrics, applications of embedded systems and recent trends in embedded systems, hardware and software design and testing, communication protocols like SPI, I2C, CAN etc.

#### Unit-2
**System Architecture:**
RISC & CISC Processor comparison, Introduction to ARM7 core architecture, ARM7 core extensions, ARM Processor families, Pipeline, memory management, Bus architecture.

#### Unit-3
**ARM instruction set & On chip Peripherals:**
ARM instruction set, thumb Instruction set, Study of on-chip peripherals like I/O ports, timers, interrupts, on-chip ADC, DAC, RTC modules, WDT, PLL, PWM, USB, I2C etc. (Use 2148/2368/2378 as reference micro-controllers)

#### Unit-4
**Interfacing and Programming:**
Basic embedded C programs for on-chip peripherals studied in system architecture. Need of interfacing, interfacing techniques, interfacing of different displays including LEDs, Graphic LCD, interfacing of input devices including Keyboard, touch screen etc.

#### Unit-5
**Real Time Operating System Concept:**
RTOS services in contrast with Traditional OS, Architecture of kernel, task scheduler, ISR, Semaphores, mailbox, message queues, events, memory management.

#### Unit-6
**Introduction to UCOS II:**
Introduction to Ucos II RTOS, Use of UCOSII, UCOS service functions like time delay
functions, semaphore related functions etc., porting of RTOS. Case study of Digital Thermometer, Smart cards

**Text/Reference Books:**
1. “Embedded Systems”, Rajkamal, TMH
2. “Embedded systems software primer”, David Simon, Pearson
5. “Embedded / real time system”, DR. K.V.K.K. Prasad, Dreamtech
8. ”ARM System Developers Guide”, Andrew Sloss
9. ”Introduction to Embedded Systems”, KV Shibu TMH
10. Embedded System Design: A Unified Hardware / Software Introduction

**Practical:**
**EXD-422 List of Experiments.**

The practical examination will be of three hours duration. It will consist of one experiment conducted during the course and an Oral examination based on the syllabus.

1. LED Patterns: Generate any four random patterns on LED Matrix.
2. Square wave Generation: ARM using timer function.
3. ARM to PC Communication via UART Transmit a message via UART of ARM and display it on Terminal of PC.
4. Decimal Counter and Multiplexing the Output: Implement a decimal counter, which counts from 0 to 99 on SSD?
5. Keyboard interfacing Sense key and display the appropriate code on SSD.
6. Stepper Motor Interfacing
8. LCD Interface: Interface LCD with ARM using only 4 pins
9. IR Remote Control Receiver: Implement IR remote control receiver using ARM
10. Implementation of simple calculator using ARM 7TDMI: with keyboard and LCD display Interface.

**Section A: Unit 1, 2, 3**
**Section B: Unit 4,5,6**

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
### SEMESTER-I

<table>
<thead>
<tr>
<th>Course Code</th>
<th>Course Title</th>
<th>Teaching Scheme: 4Hrs/week</th>
<th>Practical: 2Hrs/week</th>
<th>Examination Scheme</th>
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<tbody>
<tr>
<td>EXD-403</td>
<td>VLSI Design</td>
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<td>Theory Examination : 80 Marks</td>
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<td></td>
<td>Term Work: -----</td>
</tr>
</tbody>
</table>

### Objective:

1. To make the students able to understand the concepts of VLSI design and to design various analog systems including data converters- CMOS.
2. Amplifiers- Comparators and Switched Capacitor Circuits and optimize them with respect to different constraints: size (cost), speed, power dissipation, and reliability.
3. To acquaint the Students with bottom-up and a top-down design view of Mixed Signal Electronic Systems by the use of modern Computer Aided.

#### Unit-1

**Introduction to VHDL:**
Introduction to integrated circuit technology, The integrated circuit era, Moore’s law, VLSI Design flow, Introduction to EDA tools, VHDL Design Units, Basic language Elements, Architecture modeling styles: Behavioural modeling, Dataflow modeling, Structural modeling. Comparison of various Hardware Description Languages.

#### Unit-2

**Circuit Design using CPLD & FPGA:**
Function, procedures, Attributes, Test benches, Packages and configurations, The State diagram, modeling in VHDL with examples such as counters, Registers and Bidirectional bus. CPLD, FPGA, Comparison of CPLD & FPGA, Architecture of XC9500 CPLD Family and XC4000 FPGA Family.

#### Unit-3

**Fault Tolerance and Testability:**
Types of fault, stuck-Open and Stuck-short faults, stuck at 1& 0 fault, Fault coverage. Need of Design for testability, Testability, Design-for -testability, controllability and absorbability, Boundary Scan check, JTAG technology, TAP controller and TAP controller state diagram, Scan path, Full and partial scan.

#### Unit-4

**Introduction to CMOS:**
Introduction to MOS Technology, I – V Characteristics of NMOS and PMOS, CMOS Inverter, voltage transfer curve, Velocity saturation & Mobility degradation, Channel Length Modulation, body effect, Subthreshold Conduction, velocity saturation, junction leakage, Tunneling, Static and dynamic dissipations, Power delay product. Noise margin, Detailed analysis of CMOS Inverter with parasitic.

#### Unit-5

**CMOS Design:**
CMOS Logic families: Static & Dynamic, Ratio Circuits, Pass transistor logic. CPL, Combinational logic design, Transmission gate, design using pass transistor logic, design using TGs.
Unit-6

Fabrication and Layout:
Basic CMOS Technology: Self aligned CMOS process, N well, P well, Twin tub, Layout of CMOS Inverter, CMOS Layout and Design rules.

Text Books
7. VLSI Design Black Book, Prasad

Reference Books
2. Boyce and Baker “CMOS” EEE Press.
3. Xilinx FPGA /CPLD Data Book

Practical Examination :
EXD-423 List of Experiments

The practical examination will be of three hours duration. It will consist of one experiment conducted during the course and an Oral examination based on the syllabus.

1. Introduction to VLSI Lab (XILINX ISE,Microwind Tools,VHDL,Verolog code)
2. Design and implementation of logic gates (AND,OR,NOT,NAND,XOR,XNOR)
3. Design and implementation of Adder (H.A,Full adder by H.A, 4 Bit adder)
4. Design and implementation of MUX, DEMUX, and DECODER using data flow modeling.
5. Design and implementation of DECODER using data flow modeling.
6. Design and implementation of FF (SR,JK,)
7. Design and implementation of FF (D,T)
8. Design and implementation of COUNTER
9. Layout design of PMOS, NMOS using microwind

Section A: Unit 1, 2, 3
Section B: Unit 4,5,6

PATTERN OF QUESTION PAPER
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

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<table>
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<th>Unit-1</th>
<th>Introduction to Microwave Transmission Lines and Components:</th>
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<tr>
<td></td>
<td>History of Microwaves, Microwave Frequency bands. WAVEGUIDES: Introduction, comparison with transmission lines, propagation in TE &amp; TM mode, rectangular wave guide, TEM mode in rectangular wave guide, cut off frequency, characteristic impedance, introduction to circular waveguides and planar transmission lines. Introduction to Scattering Parameters. Microwave Passive Components: Directional Coupler, Power Divider, tees, attenuator, resonator, Isolators, circulators along with S matrix.</td>
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<td>08</td>
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<table>
<thead>
<tr>
<th>Unit-2</th>
<th>Active Microwave Semiconductor Devices and Tubes:</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Microwave Semiconductor Devices: Gunn Diodes (Gunn Effect, operation, modes of operation, microwave generation and amplification), tunnel diode (Tunneling, tunnel diode Amplifier and Oscillator), IMPATT diodes, Varactor diodes, Parametric Amplifiers Microwave Tubes: Klystron (Two and multi cavity klystron), reflex klystron, traveling wave tube, microwave crossed field tubes - magnetron (operation, characteristics and applications)</td>
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<tr>
<th>Unit-3</th>
<th>Microwave Measurements:</th>
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<thead>
<tr>
<th>Unit-4</th>
<th>Fundamentals of Radar:</th>
</tr>
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<tbody>
<tr>
<td></td>
<td>Block diagram of radar, radar equation, radar frequencies, applications of radar, Detection of Signals in Noise, Probability of Detection and false alarm, Integration of pulses, Radar cross-section of targets, cross-section fluctuations, PRFs and Range Ambiguities, Antenna parameters, System losses and propagation effects. Noise figure, radar mixers, Duplexers, A scope and PPI display, Matched Filters</td>
</tr>
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<td>08</td>
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</tbody>
</table>
### Unit-5

**MTI and Pulse Doppler Radar:**
Introduction to Doppler and MTI radar, Delay line cancellers, MTI Improvement factor, Staggered PRFs, Doppler Filter banks, Digital MTI processing, Limitations to MTI performance, AMTI, Pulse Doppler Radar, Sub clutter Visibility, Non-coherent MTI radar.

### Unit-6

**Antenna Scanning and Tracking:**
Mono pulse tracking, conical scan and sequential lobbing, low angle tracking, phased array, planner array, Limitations to tracking accuracy.

### Text Books:
1. Liao S. Y., "Microwave devices and Circuits", Prentice Hall of India

### Reference Books:
1. Rizzi P.A., "Microwave Engineering, Passive Circuits Hall of India
4. Chatterji R., Microwave Engineering, Special topics, East West Press

### EXD-424 List of Experiments: Any 8 out of the following experiments;
1. Study of microwave components.
2. To plot modes (characteristics) of reflex klystron.
3. Study of microwave Tee’s.
4. Plot V/I characteristics of Gunn oscillator.
5. Study of characteristics of Isolator and Circulator
7. Microwave power (Low/High) measurement

### Section A: Unit 1, 2, 3
### Section B: Unit 4, 5, 6

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. Five marks question will be compulsory.
### SEMESTER-I

**EXD-405 – Robotics (EC)**

<table>
<thead>
<tr>
<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
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<tbody>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
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<tr>
<td></td>
<td>Class Test : 20 Marks</td>
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<td></td>
<td>Practical/Oral : ---</td>
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<tr>
<td></td>
<td>Term Work: 25 Marks</td>
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</table>

**Objective:**
1. To study Basic concept of robotics.
2. Building block of robotics for transformation.

**Unit-1**

**Introduction:**
Automation and Robotics, Definition, Basic Structure of Robots, Classification of Robots based on co-ordinate system, Present trends and future trends in robotics, Overview of robot subsystems, Components of Robot system- Manipulator, Controller, Power conversion unit etc, Specifications of robot.

**Unit-2**

**Dynamics:**
Dynamic constraints, velocity & acceleration of moving frames, Robotic Mass Distribution & Inertia, Tension, Newton’s equation, Euler equations, Dynamic Modeling of Robotic Manipulators.

**Unit-3**

**Kinematics:**
Homogeneous co-ordinate vector operations, matrix operations, co-ordinate reference frames, Homogeneous transformation and manipulator orientation relative points reference frames, forward solutions- Link co-ordinate frames, D-H matrix, Inverse or back solutions- problem of obtaining inverse solution, techniques of using direct & geometric approach.

**Unit-4**

**End Effectors and Actuators:**
Different types of grippers, vacuum & other methods of gripping, overview of actuators, Internal & External sensors, position, relocking and acceleration sensors, proximity sensors, force sensors, touch slip laser range tender, camera.

**Unit-5**

**Motion Planning and Controllers:**
On-off trajectory, relocking and acceleration profile, Cartesian motion of manipulator, joint interpolated control, Jacobian in terms of D-H matrix, Obstacle avoidance, Basic control system, control loops of robotic system, Fuzzy controllers.

**Unit-6**

**Robot Vision:**
**Text Books:**
2. Robotic Engineering – Klafter, Thomas, Negin, PHI, New Delhi
3. Introduction to Robotics: Analysis, Control, Applications, 2ed, Niku, Wiely Publication

**Reference Books:**
4. MEMS and Microsystems Design and Manufacture- HSU, TMH, NewDelhi

**Practical Examination:**
The practical examination will be of three hours duration. It will consist of one experiment conducted during the course and an Oral examination based on the syllabus.

**Term work:**
Term work will consist of record of minimum 8 experiments out of the following list

**EXD-425 List of Experiments:**
1. Study of motion conversion (rotary to rotary, rotary to linear) using mechanical components.
2. To build robot arms using mechanical components and applying motor drive.
3. To build robot for given configuration and degrees of freedom.
4. Motion of robot for each degree of freedom. Teaching a sequence to robot using Teach Pendant.
5. To perform pick and place operation using Simulation Control Software.
6. Robot path planning using Simulation & Control Software.
8. 2D simulation of a 3 DOF robot arm. (C / C++ OR MATLAB)
9. Direct Kinematics analysis of 4-axis robot. (C / C++ OR MATLAB)

**Section A: Unit 1, 2, 3**
**Section B: Unit 4,5,6**

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
## SEMESTER-I

**EXD-441 – Artificial Neural Network & Fuzzy Logic(EL-I For ECT/E&C)**

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<thead>
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<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
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<tbody>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
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</table>

### Objective:
1. To teach the students about the concepts of artificial neural networks.
2. To study basic networks in supervised learning
3. To study basic networks in unsupervised learning
4. To teach the concept of fuzziness involved in various systems.
5. To provide adequate knowledge about fuzzy set theory.
6. To provide comprehensive knowledge of fuzzy logic control

<table>
<thead>
<tr>
<th>Unit</th>
<th>Artificial Neural Network:</th>
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<tbody>
<tr>
<td></td>
<td>Fundamental concept, Basic models of ANN, Terminologies, McCulloch-Pitts Neuron, Linear Separability</td>
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<thead>
<tr>
<th>Unit</th>
<th>Supervised Learning Network:</th>
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<tr>
<td></td>
<td>Perceptron, Adaptive Linear Neuron, Back Propagation Network Time delay Neural network</td>
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<td>Associative Memory Networks:</td>
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<td></td>
<td>Training Algorithms for pattern association, Autoassociative Memory Network, Bidirectional Associative Memory, Hopfield networks</td>
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<tr>
<th>Unit</th>
<th>Unsupervised Learning Networks:</th>
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<tr>
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<td>Fixed Weight Competitive nets, Kohonen Self Organizing Feature maps, Learning vector quantization, Counter propagation networks, Adaptive resonance theory network</td>
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<tr>
<th>Unit</th>
<th>Fuzzy Logic:</th>
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<td>Introduction, Classical sets, Fuzzy sets, Classical relations, Fuzzy relations Membership Functions: Features, Fuzzification, Methods of Membership Value Assignments</td>
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<tr>
<th>Unit</th>
<th>Defuzzification:</th>
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<tr>
<th>Unit</th>
<th>Fuzzy Decision making:</th>
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<tbody>
<tr>
<td></td>
<td>Individual Decision making, Multiperson decision making, Fuzzy Logic Control Systems</td>
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</tbody>
</table>

### Text Books
2. Timothy J. Ross, ‘Fuzzy Logic with Engineering Applications’, Third Edition Wiley India

### Reference Books
## EXD-426 List of Experiments:

1. Program to implement AND function using ADALINE with bipolar inputs and outputs
2. Program to construct and test auto associative network for input vector using HEBB rule
3. Program to implement Discrete Hopfield Network and test input pattern
4. Program to implement Kohonen self organizing feature maps for given input pattern
5. Program to implement fuzzy set operations and properties
6. Program to implement composition of Fuzzy and Crisp relations
7. Program to find union, intersection and complement of Fuzzy sets
8. Program to depict membership functions

### Section A: Unit 1, 2, 3
### Section B: Unit 4, 5, 6

**PATTERN OF QUESTION PAPER**

Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks paper:**

1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
Dr. BABASAHEB AMBEDKAR MARATHWADA UNIVERSITY, AURANGABAD
FACULTY OF ENGINEERING AND TECHNOLOGY
FINAL YEAR (EC/ECT/E&C/IE) ENGINEERING

<table>
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<tr>
<th>SEMESTER-I</th>
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<tr>
<td><strong>EXD-441 – Advanced Power Electronics(EL-I For EC)</strong></td>
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<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
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<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
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<td></td>
<td>Class Test : 20 Marks</td>
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<td>Practical/Oral : ---</td>
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<td></td>
<td>Term Work: 25 Marks</td>
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**Objective:**
1. To impart knowledge of recent and advanced developments in PE area
2. To learn the advanced applications in PE converters.
3. To introduce the topologies of energy conversion in PE.

**Unit-1**

Modern semiconductor devices and practical design consideration:
Thyristor, GTOs, IGBT, MCT their basic characteristics, power integrated circuits. Gate and base drive circuits-Design consideration for different circuits, DC-coupled circuits, isolated drive circuits and protection in drive circuits. Snubber circuit design. Temperature control and Heat sink design.

**Unit-2**

Advanced DC-DC Power Converters:

**Unit-3**

Switching DC Power Supplies:
Linear power supplies, switching power supplies, Fly back converters, Half bridge and Full bridge Converters, Forward converters, Push-Pull Converters. Protection, isolation and design criteria for SMPS.

**Unit-4**

Advanced DC-AC Power Converters:

**Unit-5**

Power Electronics Applications:

**Unit-6**

Computer Simulation of power Electronics and Control Methods:
Use of simulation tools for design and analysis, simulation of Power Electronics Circuits with Pspice, PSIM, Matlab-Simulink, Control Methods for Power Converters using sliding mode control, Fuzzy Logic control.
### Text Books/Reference Books:
5. Mohan, Power Electronics: Converters Applications and Design, Media Enhanced, 3ed, w/cd, Wiely Publication

### EXD-426 List of Experiments:
Minimum eight experiments based on above syllabus should be carried out with Hardware experiments to understand advanced power converters and developing prototype of power converters.

### Section A: Unit 1, 2, 3
### Section B: Unit 4,5,6

### PATTERN OF QUESTION PAPER
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

### For 80 Marks papers:
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
## TEACHING SCHEME

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
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<tbody>
<tr>
<td>4 Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
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<tr>
<td>Practical: 2 Hrs/week</td>
<td>Class Test : 20 Marks</td>
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<tr>
<td></td>
<td>Practical/Oral : ---</td>
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<td>Term Work: 25 Marks</td>
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## OBJECTIVE

1. Basic concepts of Cellular communication
2. Building blocks of Mobile communication
3. Traffic Routing and Grade of Service
4. Wireless Systems and Standards

## UNIT-1

### WIRELESS COMMUNICATION FUNDAMENTALS:
Introduction & Fundamental terms of communication, Evolution of Mobile Radio communication, Frequencies for radio transmission, Overview of existing technologies around the world, Cellular system, its architecture & operation.
Overview of Multiple access schemes for wireless communication - TDMA, FDMA, CDMA, SDMA.

## UNIT-2

### WIRELESS SYSTEM DESIGN CONCEPTS:
Concept of Frequency reuse & its analysis, Channel Assignment Strategies, Hand-off, its necessity & advantages, roaming, co-channel & adjacent channel interference, Trunking and Grade of Service, Improving coverage and capacity in cellular systems.

## UNIT-3

### WIRELESS NETWORKS:
Overview of 1G, 2G, 3G, 4G wireless networks, Traffic Routing in Wireless Networks, Wireless Data Services, ISDN, SS-7, PCS/PCNs, GPRS, DECT, UMTS, IMT-2000, Blue tooth, DTH.

## UNIT-4

### DIGITAL CELLULAR SYSTEMS: GSM
Features & mobile services, architecture & interfacing, signal processing, frame structure, Channels-TCH & CCH, Messaging & call processing, Message flow for MTC and MOC, Types of handover in GSM, process of Intra-MSC handover in GSM.

## UNIT-5

### WIRELESS PROTOCOLS & STANDARDS:
Protocols for network access-PRMA, Mobile IP, WAP, Wireless LAN IEEE 802.11 & its Architecture, IEEE 802.11a, 802.11b standards, IEEE802.15.4 & Zigbee.

## UNIT-6

### CDMA & MOBILE OS:
CDMA Architecture, Features & mobile services, CDMA & GSM comparison, Architecture, Mobile Operating systems- Symbian, RIM, iOS & Android features & different versions- KitKat, Jelly-Bean, Ice Cream Sandwich etc.

## TEXT BOOKS/REFERENCE BOOKS


EXD-426 List of Experiments:

**Perform any seven Experiments out of 1 to 9. Experiment No.10 is Compulsory.**

1. To Study different Multiple access techniques.
2. To Demonstrate & performs installation of GSM trainer kit.
3. To Perform Call generation and termination using AT commands.
4. To Perform sending and reading of SMS using AT command.
5. To check network availability using AT command.
6. To measure signal strength using AT commands.
7. To Demonstrate & perform installation of CDMA trainer kit.
8. To generate and transmit data with PN sequence using CDMA trainer kit.
9. To separate data and PN sequence at receiver using CDMA trainer kit.
10. To perform mini project on the basis of any one mobile OS from chapter no. 6.

Section A: Unit 1, 2, 3
Section B: Unit 4,5,6

**PATTERN OF QUESTION PAPER**

Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**

1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
Dr. BABASAHEB AMBEDKAR MARATHWADA UNIVERSITY, AURANGABAD  
FACULTY OF ENGINEERING AND TECHNOLOGY  
FINAL YEAR (EC/ECT/E&C/IE) ENGINEERING

<table>
<thead>
<tr>
<th>SEMESTER-I</th>
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<tr>
<td><strong>EXD-442 /EXD-453 – Consumer Electronics (EL-I For EC)</strong></td>
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<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
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<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
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<td>Class Test : 20 Marks</td>
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<td>Practical/Oral : 50 Marks</td>
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<td>Term Work: ----</td>
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**Objective:**
1. To acquaint students with the practical knowledge of designing and developing consumer electronic systems and products and introduce the latest trends and technologies.

<table>
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<tr>
<th><strong>Unit-1</strong></th>
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<tbody>
<tr>
<td><strong>Communication devices:</strong></td>
</tr>
<tr>
<td>Mobile handsets, Android technology, 2G,3G Mobiles, i-phone, EPABX</td>
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<tr>
<th><strong>Unit-2</strong></th>
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<tr>
<td><strong>Mass Communication devices:</strong></td>
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<tr>
<td>Colour Television, Antenna, HDTV, LCD TV, LED TV, 3D Technology In TV Interactive TV, DTH TV, Plasma TV, Video Conferencing, FAX Machine, PA System, Dolby Digital Systems, Gesture Technology In TV</td>
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<th><strong>Unit-3</strong></th>
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<tr>
<td><strong>Household electronics devices:</strong></td>
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<tr>
<td>Washing Machine, Microwave Oven, Types Applications, Electronics Weighing Balance, Air Conditioner, Vacuum Cleaner</td>
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<th><strong>Unit-4</strong></th>
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<td><strong>Printing and recording devices:</strong></td>
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<tr>
<td>LASER printer, Inkjet Printers, Photocopiers, Scanner, DVD/ CD Player, Blue ray DVD Player</td>
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<th><strong>Unit-5</strong></th>
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<td><strong>Special purpose machines:</strong></td>
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<td><strong>Security devices:</strong></td>
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<tr>
<td>Biometric Attendance Monitoring System, Working, Biometric Sensors, Home Automation System</td>
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<table>
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<tr>
<th><strong>Unit-6</strong></th>
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<tr>
<td><strong>Product Development for Consumer Electronics:</strong></td>
</tr>
<tr>
<td>Product policy; product life cycle; product classification; new product development systems; generation of new product ideas; consumer based concept screening and evaluation; economic evaluation.</td>
</tr>
<tr>
<td><strong>Compliance:</strong></td>
</tr>
<tr>
<td>Product safety and liability issues; standards related to electrical safety and standards related to fire hazards, e.g., UL and VDE. EMI/EMC requirements and design techniques for compliance, e.g. ESD, RF interference and immunity, line current harmonics and mains voltage surge.</td>
</tr>
<tr>
<td>08</td>
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</tbody>
</table>
**Text Book:**
3. Video Demisified – Kelth Jack, PI publication
5. Audio and Video system – Principles, maintenance and Troubleshooting by R.Gupta

**Reference Book:**
2. Basic TV & Video Systems - Bernard Grobb.
3. Electronic Communication Systems, Kennedy, TMH.
4. Principles of Communication Engineering - Anokh Singh-TMH.

**Term Work:** (Minimum 8 tutorials)
**EXD-473/EXD-426 List of Experiments**
Minimum 8 tutorials / assignments based on above syllabus covering all units.
- Study of CD/DVD Player.
- Study of LED/LCD Color Television.
- Fault Finding In Color Television Receiver.
- Study of Cordless Telephone.
- Study of Close Circuit Television.
- Study of Mobile Handset Trainer.
- Study of EPBAX System.
- Study of PA system.
- Study of Laser Printer.
- Study of ATM Machine.

**Section A:** Unit 1, 2, 3  
**Section B:** Unit 4, 5, 6  
**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
### SEMESTER-I

<table>
<thead>
<tr>
<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
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<tbody>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
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<td>Class Test : 20 Marks</td>
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<td>Practical/Oral : ---</td>
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<td>Term Work: 25 Marks</td>
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#### Objective:

#### Unit-1
**Transducers For Biomedical Applications:**
- Resistive transducers - Muscle force and Stress (Strain gauge), spirometry (Pententio), humidity, (Gastrin), Respiration (Thermistor)
- Inductive Transducers - Flow measurements, muscle movement (LVDT)
- Capacitive Transducers - Heart sound measurement, Pulse pick up
- Photoelectric Transducers
- Pulse transducers, Blood pressure, Piezoelectric Transducers
- Ultrasonic blood

#### Unit-2
**Bioelectric Signals, Their Recording & Machines:**
- Bioelectric signals (ECG, EMG, ECG, EOG & ERG)
- Bio-electrodes, electrodes tissue interface, contact impedance
- Effects of high contact impedance, types of electrodes, electrodes for ECG, EEG and EMG
- Physiological pre-amplifier and instrumentation amplifiers
- ECG lead systems details of ECG, EMG, and EEG machines

#### Unit-3
**Modern Imaging Systems:**
- Introduction, Basic principle & Block diagram of x-ray machine, x-ray Computed Tomography (CT)
- Magnetic resonance imaging system (NMR)
- Ultrasonic imaging system

#### Unit-4
**Patient Monitoring Systems & Audiometers:**
- Cardiac monitor, Bedside patient monitor
- Measurement of heart rate, blood pressure, temperature, respiration rate
- Arrhythmia monitor
- Methods of monitoring fetal heart rate
- Monitoring labor activity

#### Unit-5
**Therapeutic Equipments:**
- Cardiac pacemakers, cardiac defibrillators
- Hemodialysis machine, Surgical diathermy machine
- Physiotherapy: microwave Diathermy, Ultrasound therapy unit
- Electrotherapy Equipments, Ventilators

#### Unit-6
**Safety Aspects Of Medical & Computer Applications:**
- Gross current, Micro Current shock, safety standards, rays and considerations, safety testing instruments
- Biological effects of X-rays and precautions
- Use of microprocessors in medical instruments, Microcontrollers, PC based medical instruments
- Computerized Critical care units, Planning & designing a computerized critical care unit
**Text/ Reference Books:**
4. V.K. Mehta, “Electronics and Electrical Measurements” by Lesile Cromwell, PHI
5. Biomedical Instrumentation & Measurements by Lesile Cromwell, PHI

**Term Work : (Minimum 8 tutorials)**
**EXD-426 List of Experiments**

Minimum 8 tutorials / assignments based on above syllabus covering all units.
- 1- To find characteristics of thermistor and its linearization.
- 2- To find characteristics of LVDT.
- 3- To design and find gain of instrumentation amplifier.
- 4- To design and test ECG amplifier circuit.
- 5- To study X-RAY machine.
- 6- To design notch filter for EMG signals.
- 7- To study hemodialysis machine.
- 8- To study CT SCAN machine.

**Section A: Unit 1, 2, 3**
**Section B: Unit 4, 5, 6**

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
<table>
<thead>
<tr>
<th>Unit</th>
<th>Topic</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Unit-1</strong></td>
<td>Introduction to Microwave Transmission Lines and Components:</td>
</tr>
<tr>
<td></td>
<td>History of Microwaves, Microwave Frequency bands. WAVEGUIDES: Introduction, comparison with transmission lines, propagation in TE &amp; TM mode, rectangular wave guide, TEM mode in rectangular wave guide, cut off frequency, characteristic impedance, introduction to circular waveguides and planar transmission lines. Introduction to Scattering Parameters</td>
</tr>
<tr>
<td></td>
<td>Microwave Passive Components:</td>
</tr>
<tr>
<td></td>
<td>Directional Coupler, Power Divider, tees, attenuator, resonator, Isolators, circulators along with S matrix</td>
</tr>
<tr>
<td><strong>Unit-2</strong></td>
<td>Active Microwave Semiconductor Devices and Tubes:</td>
</tr>
<tr>
<td></td>
<td>Microwave Semiconductor Devices:</td>
</tr>
<tr>
<td></td>
<td>Gunn Diodes (Gunn Effect, operation, modes of operation, microwave generation and amplification), tunnel diode (Tunneling, tunnel diode Amplifier and Oscillator), IMPATT diodes, Varactor diodes, Parametric Amplifiers</td>
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<tr>
<td></td>
<td>Microwave Tubes:</td>
</tr>
<tr>
<td></td>
<td>Klystron (Two and multi cavity klystron), reflex klystron, traveling wave tube, microwave crossed field tubes - magnetron (operation, characteristics and applications)</td>
</tr>
<tr>
<td><strong>Unit-3</strong></td>
<td>Microwave Measurements:</td>
</tr>
<tr>
<td></td>
<td>Measurement of frequency, Measurement of power using calorimeter &amp; bolometer, Measurement of Impedance, Antenna measurement, Noise at microwave frequency and measurement of noise figure.</td>
</tr>
<tr>
<td></td>
<td>Modern Trends in Microwaves Engineering:</td>
</tr>
<tr>
<td></td>
<td>Effect of Microwaves on human body. Medical and Civil applications of microwaves. Electromagnetic interference / Electromagnetic Compatibility (EMI / EMC). Monolithic Microwave IC fabrication. RFMEMS for microwave components, Microwave Imaging.</td>
</tr>
<tr>
<td><strong>Unit-4</strong></td>
<td>Fundamentals of Radar:</td>
</tr>
<tr>
<td></td>
<td>Block diagram of radar, radar equation, radar frequencies, applications of radar, Detection of Signals in Noise, Probability of Detection and false alarm, Integration of pulses, Radar cross-section of targets, cross-section fluctuations, PRFs and Range Ambiguities, Antenna parameters, System losses and propagation effects. Noise figure, radar mixers, Duplexers, A scope and PPI display, Matched Filters</td>
</tr>
<tr>
<td><strong>Unit-5</strong></td>
<td>MTI and Pulse Doppler radar:</td>
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<tr>
<td></td>
<td>Introduction to Doppler and MTI radar, Delay line cancellers, MTI Improvement factor,</td>
</tr>
</tbody>
</table>
Staggered PRFs, Doppler Filter banks, Digital MTI processing, Limitations to MTI performance, AMTI, Pulse Doppler Radar, Sub clutter Visibility, Non-coherent MTI radar.

### Unit-6

**Antenna Scanning and Tracking:**
Mono pulse tracking, conical scan and sequential lobbing, low angle tracking, phased array, planner array, Limitations to tracking accuracy.

<table>
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<th>04</th>
</tr>
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</table>

**Text Books:**
1. Liao S. Y., "Microwave devices and Circuits", Prentice Hall of India

**Reference Books:**
1. Rizzi P.A., "Microwave Engineering, Passive Circuits Hall of India

**EXD-426 List of Experiments:**
Any 8 out of the following experiments;
1. Study of microwave components.
2. To plot modes (characteristics) of reflex klystron.
3. Study of microwave Tee’s.
4. Plot V/I characteristics of Gunn oscillator.
5. Study of characteristics of Isolator and Circulator
7. Microwave power (Low/High) measurement

**Section A: Unit 1, 2, 3**
**Section B: Unit 4,5,6**

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
## Dr. BABASAHEB AMBEDKAR MARATHWADA UNIVERSITY, AURANGABAD
FACULTY OF ENGINEERING AND TECHNOLOGY
FINAL YEAR (EC/ECT/E&C/IE) ENGINEERING

<table>
<thead>
<tr>
<th>SEMESTER-I</th>
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<tbody>
<tr>
<td><strong>EXD-444 – Advanced Industrial Automation(EL-I For EC/E&amp;C)</strong></td>
<td></td>
</tr>
<tr>
<td><strong>Teaching Scheme:</strong> 4Hrs/week</td>
<td><strong>Examination Scheme</strong></td>
</tr>
<tr>
<td><strong>Practical:</strong> 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td></td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/Oral : ---</td>
</tr>
<tr>
<td></td>
<td>Term Work: 25 Marks</td>
</tr>
</tbody>
</table>

### Objective:
1. The trend in the Industry for automation is changing one and student will able to develop the skill set for latest development of automation.

<table>
<thead>
<tr>
<th>Unit-1</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Basic of Automation:</strong></td>
<td></td>
</tr>
<tr>
<td>Introduction of sensors, actuators, control loop, concept of process variables, set point, controlled variable, manipulated variable, load variable. Representation of process loop components using standard symbols (basic with reference to control loop), and Examples of process loops like temperature, flow, level, pressure etc. Hierarchical levels of automation, introduction to plant automation.</td>
<td>06</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit-2</th>
<th></th>
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</thead>
<tbody>
<tr>
<td><strong>Transmitters and Converter:</strong></td>
<td></td>
</tr>
<tr>
<td>Need of transmitter (concept of field area &amp; control room area), Need for standardization of signals, current, voltage and pneumatic signal standards, concept of live &amp; dead zero, DPT, span &amp; zero adjustment, Two wire transmitters, SMART transmitter: Comparison with conventional transmitter, Block schematic. Converters: Difference between converter &amp; transmitter, Pneumatic to current converter, Current to pneumatic converter. Swtiches: Temperature, pressure, Level switch, Proximity switch, Reed switch, Contactors</td>
<td>08</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit-3</th>
<th></th>
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</thead>
<tbody>
<tr>
<td><strong>Actuators:</strong></td>
<td></td>
</tr>
<tr>
<td>Types of Control Valve, Control valve terminology Range ability, turndown, valve capacity. Air to open, Air to close, valve gain etc. Control valve characteristics: Inherent &amp; installed Control valve accessories. Positioners: Application/Need, Types, Effect on performance of control valves. Volume boosters, Pressure Boosters, Reversing relay, Solenoid valves, Air lock, position indicating switches, Electro pneumatic converter, hand wheel. Brief of stepper motor, servo-motor, Motor control circuits, AC Drives, DC Drives, VF Drives, PWM Techniques</td>
<td>06</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit-4</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Programmable Logic Controller (PLC):</strong></td>
<td></td>
</tr>
<tr>
<td>Continuous versus Discrete Process Control, ladder diagram using standard symbols, Architecture of PLC, Types of Input &amp; Output modules (AI, DI, AO, DO), Types of Timer, Counters, Interfacing pneumatic &amp; Hydraulic systems, Fixed &amp; Modular PLC (Rack, Slot, Grouping), Specifications, manufacturers, PLC ladder diagram and instructions, PLC Programming for process applications, Supervisory control system and data acquisition (SCADA): Introduction to SCADA, SCADA architecture, creation of data base, interfacing with PLC.</td>
<td>08</td>
</tr>
</tbody>
</table>
### Unit-5

**Industry Standard Protocols:**

HART Protocol introduction, frame structure, programming, implementation examples. Benefits, Advantages and Limitations. Introduction to Foundation Fieldbus H which includes structure, programming, FDS configuration, implementation examples, benefits, advantages and limitations. Comparison with other fieldbus standards like Devicenet, Profibus, Profinet, Controlnet, CAN, Industrial Ethernet etc.

| 06 |

### Unit-6

**Distributed Control Systems Basics:**

DCS introduction, functions, advantages and limitations, DCS as an automation tool to support Enterprise Resources Planning. DCS Architecture of different makes, Latest trends and developments. DCS detail engineering, specifications, configuration and programming, functions including database management, reporting, alarm management, communication, third party interface, control and display.

| 06 |

### Term Work:

1. Continuous assessment of the students in the semester
2. Satisfactory performance of laboratory experiments
3. Internal oral for the students

### References Books / Handbooks

2. Programmable Logic Controller Principles and Applications by Webb and Reis, PHI Publications
3. Distributed Computer Control for Industrial Automations by Popovik Bhatkar, Dekkar Publications
5. Introduction to Programmable Logic Controller by Garry Dunning, Thomson Learning Publications.

### EXD-426 List of Experiments:

1. Study of PLC and PLC programming
2. Study of PLC timers and counters
3. Solenoid valve sequential control using PLC.
4. Servo and Stepper Motor control using PLC systems.
5. Pneumatic control using PLC for air/gas control system.
6. PLC programming for distribution station.
7. PLC programming for PID block
8. Develop SCADA system for given application
9. Interfacing PLC to hydraulic & Pneumatic circuits.
10. Interfacing of VFD to PLC

### Section A: Unit 1, 2, 3

### Section B: Unit 4,5,6

### PATTERN OF QUESTION PAPER

Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

### For 80 Marks papers:

1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
This open elective subject syllabus can be select as per the requirement of the industry and institute with proper permission of competent authority.
The project work will be carried out by a batch of at the most 3 students (preferably 2 students) working on a topic related to the electronics and allied branches. The topic may be from one of the following.

1. Laboratory work involving constructional theoretical and design aspects of the project/system.
2. Modification aspect of existing electronics systems.
3. It can be practical need of the industry, which should involve system design aspect.
4. Survey of latest development in Electronics and allied fields. It shall consist of the term work in the form of hand written typed report not less than 25 pages.

This should include the literature survey technical details related data that is collected & design that are required for project work part-I.

The candidate shall give a seminar on the subject chosen above in the presence of Guide and External examiner preferably from industry or the university.
<table>
<thead>
<tr>
<th><strong>SEMESTER-II</strong></th>
<th><strong>EXD451 – Computer Communication Network</strong></th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Teaching Scheme:</strong></td>
<td><strong>Examination Scheme</strong></td>
</tr>
<tr>
<td>4Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/Oral : 50 Marks</td>
</tr>
<tr>
<td></td>
<td>Term Work: ----</td>
</tr>
</tbody>
</table>

**Objective:**

1. To interpret the layering concepts in computer networks.
2. To understand internals of protocols such as HTTP, FTP, SMTP, TCP, UDP, IP
3. To study different security techniques & its algorithms.

**Unit-1**

**Introduction:**
The computer-Communications Revolution, From communications to computer, From computer to communications.

**Communication Networks:**

**Computer Networking:**

**Unit-2**

**Link Perspective and Network Perspective :**
DATA LINK LAYER DESIGN ISSUES, Error detection and correction . Elementary data link protocols: A simplex stop and wait protocol, sliding window protocols. Network layer design issues, Routing algorithm, Congestion control algorithm, Quality of service

**Unit-3**

**The Transport Layer and Application Layer:**
The transport service, Elements of transport protocols, A simple transport protocol, The internet transport protocols: UDP, TCP, DNS, Electronic mail, WWW

**Unit-4**

**ISDN :**
ISDN overview, ISDN Interfaces and Functions, ISDN physical layer, ISDN Data Link Layer, ISDN Network Layer, ISDN services, Broadband ISDN

**Unit-5**

**Frame Relay and ATM**
### Unit-6

**Cryptography And Network Security:**

<table>
<thead>
<tr>
<th>Text Books</th>
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</thead>
<tbody>
<tr>
<td>4. Bansod, Computer Networks, Wiley Publication</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Reference Books</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. D. Comer, “Computer Networks and Internet TCP/IP</td>
</tr>
<tr>
<td>4. Tularam M. Bansod, “Computer Networks”, Dreamtech</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>EXD-471 List of Experiments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Study of ISO-OSI reference model</td>
</tr>
<tr>
<td>2. Study of TCP/IP reference model</td>
</tr>
<tr>
<td>3. Study of Topologies and Interconnection devices</td>
</tr>
<tr>
<td>4. Study of LAN, MAN, WANS.</td>
</tr>
<tr>
<td>5. Study of Errors and error correction techniques</td>
</tr>
<tr>
<td>6. Study of sliding window protocol</td>
</tr>
<tr>
<td>7. Study of UDP, TCP.</td>
</tr>
<tr>
<td>8. Study of DNS, WWW, Electronic mail</td>
</tr>
<tr>
<td>9. Study of architecture of ISDN</td>
</tr>
<tr>
<td>10. Study of Frame relay</td>
</tr>
<tr>
<td>11. Study of ATM</td>
</tr>
<tr>
<td>12. Write a program for encryption and description using monoalphabetic substitution or polyalphabetic substitution.</td>
</tr>
<tr>
<td>13. Write a program for PC to PC communication using RS232 port.</td>
</tr>
</tbody>
</table>

**Section A: Unit 1, 2, 3**

**Section B: Unit 4, 5, 6**

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

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1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
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<thead>
<tr>
<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
</tbody>
</table>

**Objective:**
1. Building blocks of Fiber communication
2. Traffic Routing and Grade of Service
3. Different networks Systems and Standards

**Unit-1**

**Unit-2**

**Unit-3**
Optical Fiber Losses:
Attenuation/Absorption, Scattering, Dispersion. Coupling losses. Splices and Connectors. Related numerical on above topics.

**Unit-4**
Digital FOC System:

**Unit-5**
Optical Networks:
Network Concept, Network Topologies, SONET; SDH Tracking. Photonic switching and Sensor applications. OTDR measurements, WDM network, Passive optical Networks, optical Ethernet. Related numerical on above topics.

**Unit-6**
Performance Measurement and Monitoring
Measurement Standards, Basic Test Equipment, Optical Power Measurement, Optical fibre characteristics, Eye Design Test, Optical Time Domain Reflectometer, optical Performance Monitoring, optical Fiber System Performance Measurement

**Text/Reference Books:**
1. Optical Fiber Communication - Keiser (McGraw Hill)
2. Fiber Optic Communication - Agrawal (Khanna)
3. Optical Fiber Communication - Senior (PHI)
4. Optical Fibers and Fiber Optic Communication System - Sarkar (S. Chand)
5. Optical Communications - Barapte (Technova)
EXD-472 List of Experiments
It will consist of a record of 10 experiments based on above syllabus.
1. Measurement of insertion loss and isolation loss of three port circulator.
2. To plot electrical characteristics of source and Detector
3. Numerical Aperture measurement of fiber
4. Attenuation Measurement of fiber
5. Eye pattern Measurement
6. BER measurement.
7. Losses measurement in optical fiber

<table>
<thead>
<tr>
<th>Section A: Unit 1, 2, 3</th>
<th>Section B: Unit 4,5,6</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>PATTERN OF QUESTION PAPER</strong></td>
<td></td>
</tr>
<tr>
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<td></td>
</tr>
<tr>
<td><strong>For 80 Marks papers:</strong></td>
<td></td>
</tr>
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<td>1. Section A &amp; Section B should be of 40 marks each.</td>
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<tr>
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<td></td>
</tr>
<tr>
<td>4. 10 marks question will be compulsory.</td>
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</tbody>
</table>
### Teaching Scheme: 4Hrs/week  
#### Practical: 2Hrs/week

<table>
<thead>
<tr>
<th>Semester</th>
<th>EXD-453 – Consumer Electronics (ECT/E&amp;C)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Examination Scheme</td>
</tr>
<tr>
<td></td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td></td>
<td>Class Test : 20 Marks</td>
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<tr>
<td></td>
<td>Practical/Oral : 50 Marks</td>
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<tr>
<td></td>
<td>Term Work: ----</td>
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</tbody>
</table>

#### Objective:

2. To acquaint students with the practical knowledge of designing and developing consumer electronic systems and products and introduce the latest trends and technologies.

<table>
<thead>
<tr>
<th>Unit</th>
<th>Communication devices:</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Mobile handsets, Android technology, 2G,3G Mobiles, i-phone, EPABX</td>
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<thead>
<tr>
<th>Unit</th>
<th>Mass Communication devices:</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Colour Television, Antenna, HDTV, LCD TV,LED TV, 3D Technology In TV Interactive TV, DTH TV, Plasma TV, Video Conferencing, FAX Machine, PA System, Dolby Digital Systems, Gesture Technology In TV</td>
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<thead>
<tr>
<th>Unit</th>
<th>Household electronics devices:</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Washing Machine, Microwave Oven, Types Applications, Electronics Weighing Balance, Air Conditioner, Vacuum Cleaner</td>
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<thead>
<tr>
<th>Unit</th>
<th>Printing and recording devices:</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>LASER printer, Inkjet Printers, Photocopiers, Scanner, DVD/CD Player, Blue ray DVD Player</td>
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<thead>
<tr>
<th>Unit</th>
<th>Special purpose machines:</th>
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<tr>
<th>Unit</th>
<th>Security devices:</th>
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<tbody>
<tr>
<td></td>
<td>Biometric Attendance Monitoring System, Working, Biometric Sensors, Home Automation System</td>
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<thead>
<tr>
<th>Unit</th>
<th>Product Development for Consumer Electronics:</th>
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<tbody>
<tr>
<td></td>
<td>Product policy; product life cycle; product classification; new product development systems; generation of new product ideas; consumer based concept screening and evaluation; economic evaluation.</td>
</tr>
</tbody>
</table>

#### Compliance:

Product safety and liability issues; standards related to electrical safety and standards related to fire hazards, e.g., UL and VDE. EMI/EMC requirements and design techniques for compliance, e.g. ESD, RF interference and immunity, line current harmonics and mains voltage surge.
**Text Book:**
9. Video Demisified –Keith Jack, PI publication
10. Audio & Video Systems-R.G.Gupta
11. Audio and Video system – Principles, maintenance and Troubleshooting by R.Gupta

**Reference Book:**
11. Electronic Communication Systems, Kennedy, TMH.

**Term Work : (Minimum 8 tutorials)**
**EXD-473 List of Experiments**
Minimum 8 tutorials / assignments based on above syllabus covering all units.
11. Study of CD/DVD Player.
14. Study of Cordless Telephone.
17. Study of EPBAX System.
18. Study of PA system.

**Section A: Unit 1, 2, 3**
**Section B: Unit 4,5,6**

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**For 80 Marks papers:**
5. Section A & Section B should be of 40 marks each.
6. Five questions in each section.
7. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
8. 10 marks question will be compulsory.
### SEMESTER-II

**EXD-454 – Applied Digital Signal Processing (EC)**

<table>
<thead>
<tr>
<th>Teaching Scheme:</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>4Hrs/week</td>
<td>Theory Examination: 80 Marks</td>
</tr>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/Oral : 50 Marks</td>
</tr>
<tr>
<td></td>
<td>Term Work: ---</td>
</tr>
</tbody>
</table>

**Objective:**

1) To study different digital signal and filter techniques & its algorithms
2) To enhance the knowledge of digital signal which is a broad area of research and development

### Unit-1

**Multirate Signal processing:**
- Sampling rate reduction: decimation by integer factors
- Sampling rate increase: interpolation by integer factors
- Multistage decimation and multistage interpolation
- Polyphase filter structures
- Two channel quadrature mirror filter bank

08

### Unit-2

**Adaptive filters:**
- Need of adaptive filters, adaptive filters as noise cancellation
- Configuration of adaptive filters, main components of adaptive filters
- Adaptive Algorithms: LMS adaptive algorithm and implementation, recursive least square algorithms

08

### Unit-3

**Linear prediction and optimum linear filters:**
- Lattice structures, AR, MA & ARMA
- Forward & backward linear prediction, Different approaches for LPCs: Autocorrelation method, Covariance method, Lattice structure method

04

### Unit-4

**Power Spectrum Estimation:**
- Characterization of random signals: review of deterministic signals, random signals, correlation function, power spectra, DT random signals
- Estimation of autocorrelation and power spectrum of random signal, Non parametric methods for power spectrum estimation- Bartlett window and Welch method

08

### Unit-5

**Architectures for DSPs:**
- Different types of Architectures for DSPs, Circular buffering, MAC unit, Barrel shifter, special instructions, on chip memory, Fixed and Floating point representations, Selection of DSPs, case study of TMS320c54XX, Implementation of basic algorithms like FIR, IIR Filters
- Introduction to SHARC processor, VLSI architecture of DSP algorithms

06

### Unit-6

**Applications of DSP:**
- Applications of multirate signal processing, applications of adaptive filters in biomedical (EEG, ECG), radar, speech and telephone
- Applications of DSP in audio systems, image processing and communication

06

**Text Books:**

5. Microprocessors with examples from TMS 320C54XX”, Thomas Publication.

Reference Books:
1. P. P. Vaidyanathan, “Multirate Systems and filter banks”, PHI.
6. www dspguide.com
10. Applications to DSP Using Matlab by Proakis

EXD-474 List of Experiments:
Practical exam will consist of record of minimum 8 practicals out of the following using matlab.
1. Generate random signals and plot their realization.
2. Implementation of Least Mean Square (LMS) Algorithm.
3. Determination of FIR prediction filters using Forward and Backward prediction.
4. To implement Levinson Durbin Algorithm for solution of Normal equations.
5. Realization of cascade Lattice of FIR Filter.
7. Demonstration of Hardware and Software utilities for DSP starter kits (Texas, ADSP or Motorola).
8. Implementation of any one application of the following DSP Algorithms on DSP processors or Matlab: Implementation of FIR Filter. Implementation of IIR Filter

Section A: Unit 1, 2, 3
Section B: Unit 4,5,6

PATTERN OF QUESTION PAPER
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

For 80 Marks papers:
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
### SEMESTER-II

**EXD-491 – Antenna Theory & Wave Propagation (EL-II For ECT/E&C)**

<table>
<thead>
<tr>
<th>Teaching Scheme:</th>
<th>4Hrs/week</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Practical:</td>
<td>2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
</tbody>
</table>

**Objective:**

**Unit-1**

**Fundamental Concepts:**

Definition of antenna And classification of antennas, Physical concept of radiation, Radiation pattern, near-and far-field regions, reciprocity, beam width and band width, directivity and gain, effective aperture, polarization, input impedance, efficiency, Friis transmission equation, radiation integrals and auxiliary potential functions.

**Unit-2**

**Radiation from Wires and Loops:**

Infinitesimal dipole, finite-length dipole, linear elements near conductors, dipoles for mobile communication, small circular loop.

**Unit-3**

**Aperture and Reflector Antennas:**

Huygens' Principle, radiation from rectangular and circular apertures, design considerations, Babinet's principle, Radiation from sectoral and pyramidal horns, design concepts, prime-focus parabolic reflector and cassegrain antennas.

**Unit-4**

**Broadband Antennas:**

Log-periodic and Yagi antennas, frequency independent antennas, broadcast antennas, necessity and working, feeding techniques of Antenna Arrays and basic concepts of Smart Antennas.

**Unit-5**

**Microstrip Antennas:**

Basic characteristics of microstrip antennas, different feeding methods, methods of analysis, design of rectangular, traingular and circular patch antennas. Excitation modes of patch antenna, Radiation mechanism of patch antennas. Advantages & Limitations of Patch antenna, numericals based on design equations.

**Unit-6**

**Radio Wave Propagation:**

**Sky Wave Propagation:** Structure of the ionosphere – Effective dielectric constant of ionized region – Mechanism of refraction – Refractive index – Critical frequency – Skip distance – Effect of earth’s magnetic field – Energy loss in the ionosphere due to collisions – Maximum usable frequency (numerical) – Fading and diversity reception.

**Space Wave Propagation:** Reflection from ground for vertically and horizontally polarized waves – Reflection characteristics of earth – Resultant of direct and reflected ray at the receiver – Duct propagation.

**Ground Wave Propagation:** Attenuation characteristics for ground wave propagation – Calculation of field strength at a distance.
**TEXT/REFERENCE BOOKS:**

**TERM WORK: (Minimum 8 tutorials)**

**EXD-475 List of Experiments:**

| Minimum 8 tutorials / assignments based on above syllabus covering all units. |

**Section A: Unit 1, 2, 3**
**Section B: Unit 4, 5, 6**

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
### SEMESTER-II

**EXD-491 – Biomedical Electronics(EL-II For EC)**

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>4Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/Oral : ---</td>
</tr>
<tr>
<td></td>
<td>Term Work: 25 Marks</td>
</tr>
</tbody>
</table>

#### Objective:

#### Unit-1

**Transducers For Biomedical Applications:**
- Resistive transducers - Muscle force and Stress (Strain-gauge), spirometry (Potentiometer), humidity, (Gasstrans), Respiration (Thermistor)
- Inductive Transducers - Flow measurements, muscle movement (LVDT)
- Capacitive Transducers - Heart sound measurement, Pulse pickup
- Photoelectric Transducers - Pulse transducers, Blood pressure, Piezoelectric Transducers

#### Unit-2

**Bioelectric Signals, Their Recording & Machines:**
- Bioelectric signals (ECG, EMG, ECG, EOG & ERG) and their characteristics
- Bio-electrodes, electrodes tissue interface, contact impedance, effects of high contact impedance, types of electrodes, electrodes for ECG, EEG and EMG
- Physiological pre-amplifier and instrumentation amplifiers
- ECG lead systems details of ECG, EMG, and EEG machines

#### Unit-3

**Modern Imaging Systems:**
- Introduction, Basic principle & Block diagram of x-ray machine, x-ray Computed Tomography (CT), Magnetic resonance imaging system (NMR), ultrasonic imaging system

#### Unit-4

**Patient Monitoring Systems & Audiometers:**
- Cardiac monitor, Bedside patient monitor, measurement of heart rate, blood pressure, temperature, respiration rate, Arrhythmia monitor, Methods of monitoring fetal heart rate, Monitoring labor activity

#### Unit-5

**Therapeutic Equipments:**
- Cardiac pacemakers, cardiac defibrillators, Hemodialysis machine, Surgical diathermy machine, Physiotherapy: microwave Diathermy, Ultrasound therapy unit
- Electrotherapy Equipments, Ventilators

#### Unit-6

**Safety Aspects Of Medical & Computer Applications:**
- Gross current, Micro Current shock, safety standards rays and considerations, safety testing instruments, biological effects of X-rays and precautions
- Use of microprocessors in medical instruments, Microcontrollers, PC based medical instruments
- Computerized Critical care units, Planning & designing a computerized critical care unit
Text/Reference Books:
4. V.K. Mehta, “Electronics and Electrical Instrumentaion”
5. Biomedical Instrumentation & Measurements by Lesile Cromwell, PHI

TERM WORK: (Minimum 8 tutorials)
EXD-475 List of Experiments:
Minimum 8 tutorials / assignments based on above syllabus covering all units.

1. To find characteristics of thermistor and its linearization.
2. To find characteristics of LVDT.
3. To design and find gain of instrumentation amplifier.
4. To design and test ECG amplifier circuit.
5. To study X-RAY machine.
6. To design notch filter for EMG signals.
7. To study hemodialysis machine.
8. To study CT SCAN machine.

Section A: Unit 1, 2, 3
Section B: Unit 4, 5, 6

PATTERN OF QUESTION PAPER
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

For 80 Marks papers:
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
**SEMESTER-II**

**EXD-492 – Applied Digital Signal Processing (EL-II for ECT/E&C)**

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>4Hrs/week</td>
<td>Theory Examination: 80 Marks</td>
</tr>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Class Test: 20 Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/Oral: ---</td>
</tr>
<tr>
<td></td>
<td>Term Work: ---</td>
</tr>
</tbody>
</table>

**Objective:**

**Unit-1**

**Multirate Signal processing:**
- Sampling rate reduction: decimation by integer factors
- Sampling rate increase: interpolation by integer factors
- Multistage decimation and multistage interpolation
- Polyphase filter structures, two channel quadrature mirror filter bank.

| 08 |

**Unit-2**

**Adaptive filters:**
- Need of adaptive filters, adaptive filters as noise cancellation
- Configuration of adaptive filters
- Main components of adaptive filters

| 08 |

**Unit-3**

**Linear prediction and optimum linear filters:**
- Lattice structures, AR, MA & ARMA
- Forward & backward linear prediction
- Different approaches for LPCs: Autocorrelation method, Covariance method, Lattice structure method.

| 04 |

**Unit-4**

**Power Spectrum Estimation:**
- Characterization of random signals: review of deterministic signals, random signals, correlation function, power spectra, DT random signals
- Estimation of autocorrelation and power spectrum of random signal
- Non parametric methods for power spectrum estimation: Bartlett window and Welch method.

| 08 |

**Unit-5**

**Architectures for DSPs:**
- Different types of Architectures for DSPs, Circular buffering, MAC unit, Barrel shifter
- Special instructions, on chip memory, Fixed and Floating point representations
- Selection of DSPs, case study of TMS320c54XX, Implementation of basic algorithms like FIR, IIR Filters.

| 06 |

**Unit-6**

**Applications of DSP:**
- Applications of multirate signal processing, applications of adaptive filters in biomedical (EEG,ECG), radar, speech and telephone
- Applications of DSP in audio systems, image processing and communication.

| 06 |

**Text Books:**

### Reference Books:
1. P. P. Vaidyanathan, “Multirate Systems and filter banks”, PHI.
6. www dspguide.com
9. Applications to DSP Using Matlab by Proakis

### EXD-475 List of Experiments:
Practical exam will consist of record of minimum 8 practicals out of the following using matlab.
1. Generate random signals and plot their realization.
2. Implementation of Least Mean Square (LMS) Algorithm.
3. Determination of FIR prediction filters using Forward and Backward prediction.
4. To implement Levinson Durbin Algorithm for solution of Normal equations.
5. Realization of cascade Lattice of FIR Filter.
7. Demonstration of Hardware and Software utilities for DSP starter kits (Texas, ADSP or Motorola).
8. Implementation of any one application of the following DSP Algorithms on DSP processors or Matlab: Implementation of FIR Filter.
9. Implementation of IIR Filter

### Section A: Unit 1, 2, 3
### Section B: Unit 4, 5, 6

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
<table>
<thead>
<tr>
<th>Semester-II</th>
<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>EXD-492 – Wireless Mobile Communication(EL-II For EC)</td>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Practical/Oral : ---</td>
</tr>
<tr>
<td></td>
<td></td>
<td>Term Work: ---</td>
</tr>
</tbody>
</table>

**Objective:**

**Unit-1**

**Wireless Communication Fundamentals:**
Introduction & Fundamental terms of communication, Evolution of Mobile Radio communication, Frequencies for radio transmission, Overview of existing technologies around the world, Cellular system, its architecture & operation.
Overview of Multiple access schemes for wireless communication -TDMA, FDMA, CDMA, SDMA.

**Unit-2**

**Wireless System Design Concepts:**
Concept of Frequency reuse & its analysis, Channel Assignment Strategies, Hand-off, its necessity & advantages, roaming, co-channel & adjacent channel interference, Trunking and Grade of Service, Improving coverage and capacity in cellular systems.

**Unit-3**

**Wireless Networks:**
Overview of 1G, 2G, 3G, 4G wireless networks, Traffic Routing in Wireless Networks, Wireless Data Services, ISDN, SS-7, PCS/PCNs, GPRS, DECT, UMTS, IMT-2000, Blue tooth, DTH.

**Unit-4**

**Digital Cellular Systems: GSM**
Features & mobile services, architecture & interfacing, signal processing, frame structure, Channels-TCH & CCH, Messaging & call processing, Message flow for MTC and MOC, Types of handover in GSM, process of Intra-MSC handover in GSM.

**Unit-5**

**Wireless Protocols & Standards:**
Protocols for network access-PRMA, Mobile IP, WAP, Wireless LAN IEEE 802.11 & its Architecture, IEEE 802.11a, 802.11b standards, IEEE802.15.4 & Zigbee.

**Unit-6**

**CDMA & Mobile OS:**
CDMA Architecture, Features & mobile services, CDMA & GSM comparison, Architecture, Mobile Operating systems- Symbian, RIM, iOS & Android features & different versions-KitKat, JellyBean, Ice Cream Sandwich etc.

**Reference Books:**
EXD-475 **List of Experiments:**
Perform any seven Experiments out of 1 to 9. Experiment No.10 is Compulsory.

1. To Study different Multiple access techniques.
2. To Demonstrate & performs installation of GSM trainer kit.
3. To Perform Call generation and termination using AT commands.
4. To Perform sending and reading of SMS using AT command.
5. To check network availability using AT command.
6. To measure signal strength using AT commands.
7. To Demonstrate & perform installation of CDMA trainer kit.
8. To generate and transmit data with PN sequence using CDMA trainer kit.
9. To separate data and PN sequence at receiver using CDMA trainer kit.
10. To perform mini project on the basis of any one mobile OS from chapter no. 6.

**Section A: Unit 1, 2, 3**
Section B: Unit 4,5,6

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
# SEMESTER-II

**EXD-493 – Robotics (EL-II For ECT/E&C)**

<table>
<thead>
<tr>
<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td></td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/Oral : ---</td>
</tr>
<tr>
<td></td>
<td>Term Work: ---</td>
</tr>
</tbody>
</table>

**Objective:**
1. To study Basic concept of robotics.
2. Building block of robotics for transformation.

## Unit-1
**Introduction:**
Automation and Robotics, Definition, Basic Structure of Robots, Classification of Robots based on co-ordinate system, Present trends and future trends in robotics, Overview of robot subsystems, Components of Robot system- Manipulator, Controller, Power conversion unit etc, Specifications of robot.

## Unit-2
**Dynamics:**
Dynamic constraints, velocity & acceleration of moving frames, Robotic Mass Distribution & Inertia, Tension, Newton’s equation, Euler equations, Dynamic Modeling of Robotic Manipulators.

## Unit-3
**Kinematics:**
Homogeneous co-ordinate vector operations, matrix operations, co-ordinate reference frames, Homogeneous transformation and manipulator orientation relative points reference frames, forward solutions- Link co-ordinate frames, D-H matrix, Inverse or back solutions- problem of obtaining inverse solution, techniques of using direct & geometric approach.

## Unit-4
**End Effectors and Actuators:**
Different types of grippers, vacuum & other methods of gripping, overview of actuators, Internal & External sensors, position, relocking and acceleration sensors, proximity sensors, force sensors, touch slip laser range tinder, camera.

## Unit-5
**Motion Planning and Controllers:**
On-off trajectory, relocking and acceleration profile, Cartesian motion of manipulator, joint interpolated control, Jacobian in terms of D-H matrix, Obstacle avoidance, Basic control system, control loops of robotic system, Fuzzy controllers.

## Unit-6
**Robot Vision:**

**Text Books/ Reference Books:**
2. Robotic Engineering – Klaffer, Thomas, Negin, PHI, New Delhi
6. MEMS and Microsystems Design and Manufacture- HSU, TMH, NewDelhi
Practical Examination:
The practical examination will be of three hours duration. It will consist of one experiment conducted during the course and an Oral examination based on the syllabus.

Term work:
Term work will consist of record of minimum 8 experiments out of the following list

EXD-475 List of Experiments:

1. Study of motion conversion (rotary to rotary, rotary to linear) using mechanical components.
2. To build robot arms using mechanical components and applying motor drive.
3. To build robot for given configuration and degrees of freedom.
4. Motion of robot for each degree of freedom. Teaching a sequence to robot using teach Pendant.
5. To perform pick and place operation using Simulation Control Software.
6. Robot path planning using Simulation & Control Software.
8. 2D simulation of a 3 DOF robot arm. (C / C++ OR MATLAB)
9. Direct Kinematics analysis of 4-axis robot. (C / C++ OR MATLAB)

Section A: Unit 1, 2, 3
Section B: Unit 4, 5, 6

PATTERN OF QUESTION PAPER
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

For 80 Marks papers:

1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
**Objective:**
1. To understand the basics of an electrical drive system.
2. To design and analyze simple drive systems.
3. To understand concepts of convertors and chopper.
4. To understand concepts of invertors and PWM techniques.
5. To understand the importance of suppressing current harmonics generated in single phase utility system.

<table>
<thead>
<tr>
<th>Unit-1 Introduction to Motor Drives</th>
<th>04</th>
</tr>
</thead>
<tbody>
<tr>
<td>Classification of Electric Drives, Requirements of Electric Drives, Criteria for selecting drive components.</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit-2 Converters and Control</th>
<th>06</th>
</tr>
</thead>
<tbody>
<tr>
<td>Phase controlled convertors, Four quadrant operation, Choppers, AC to DC converters</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit-3 DC motor drives</th>
<th>06</th>
</tr>
</thead>
<tbody>
<tr>
<td>Speed-torque characteristics DC shunt, PMDC and series motors, Dynamic model Speed and position control methods</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit-4 Inverters and PWM techniques</th>
<th>08</th>
</tr>
</thead>
<tbody>
<tr>
<td>Voltage Source Inverters, Current Source Inverters, PWM techniques, sine-triangle comparison, harmonic elimination, hysteresis current controllers, space vector PWM</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit-5 AC motor drives</th>
<th>10</th>
</tr>
</thead>
<tbody>
<tr>
<td>d-q model of induction motor, constant flux speed control structure, vector control model, vector control structure</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Unit-6 Optimizing the Utility Interface with Power Electronic Systems</th>
<th>06</th>
</tr>
</thead>
</table>

**Text Books:**
1. Power Electronics Mohan Undeland Riobbins Wiley India
2. Power Electronics L Umanand Wiley India
3. Modern Power Electronics & AC Drives, Bimal K Bose, Pearson Education

**Reference Books:**
1. Power Electronic Control of AC motors, Murphy and Turnbull Pergamon Press

**Practical Examination:**
The practical examination will be of three hours duration. It will consist of one experiment conducted during the course and an Oral examination based on the syllabus.

**Term work:**
Term work will consist of record of minimum 8 experiments out of the following list

EXD-475 List of Experiments:
1.
2.
3.
4.

Section A: Unit 1, 2, 3
Section B: Unit 4, 5, 6

PATTERN OF QUESTION PAPER
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

For 80 Marks papers:
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
## SEMESTER-II

<table>
<thead>
<tr>
<th>Course Code</th>
<th>Course Title</th>
<th>Teaching Scheme</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>EXD-494</td>
<td>Satellite Communication (EL-II For EC/ECT/E&amp;C)</td>
<td>4Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2Hrs/week</td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Practical/Oral : ---</td>
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<tr>
<td></td>
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<td>Term Work: 50 Marks</td>
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</tbody>
</table>

### Objective:

<table>
<thead>
<tr>
<th>Unit</th>
<th>Objective</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>An overview of satellite communication, Satellite Orbits, Kepler's three laws of planetary Motion, governing satellite motion, orbital parameters, orbital perturbations, Doppler effects, geostationary orbit, antenna look angles, antenna mount, limits of visibility, Earth eclipse of satellite, sun transit outage, inclined orbits, sun-synchronous orbit, launching of geostationary satellites</td>
</tr>
<tr>
<td>2</td>
<td>Introduction ,atmospheric loss, inospheric effects, rain attenuation, other impairments, Space Link : Introduction, EIRP, transmission losses, link power budget, system noise, CNR, uplink, down link, effects of rain, Combined CNR, Noise figure and Noise Temperature, G/T Ratio.</td>
</tr>
<tr>
<td>3</td>
<td>Single access, Pre-assigned FDMA, Demand Assigned FDMA ,SPADE system, FDMA downlink analysis, SCPC, TDMA, reference burst, frame efficiency and channel capacity, pre-assigned TDMA, Demand Assigned TDMA, Downlink analysis for digital transmission, comparison of uplink power requirements for FDMA and TDMA, CDMA, Direct sequence spread spectrum, PN system ,spread spectrum and disspreading, CDMA throughput, SDMA</td>
</tr>
<tr>
<td>4</td>
<td>Introduction, power supply units, altitude control, station keeping, thermal control, TT&amp;C, Transponders, antenna subsytems.</td>
</tr>
<tr>
<td>5</td>
<td>Types of Earth stations, Earth station antennas, Tracking, Equipment for earth stations, Equipment Reliability and Space qualification.</td>
</tr>
<tr>
<td>6</td>
<td>INTELSAT Series ,INSAT, INMARSAT, LEO, MEO,VSAT, DBS Television and Radio, Remote sensing, Mobile satellite services: GSM and GPS, Satellite navigation system, DTH ,GRAMSAT, Weather forecasting satellites, Scientific satellites, An Introduction to Non Geostationary Orbit Satellite Systems</td>
</tr>
</tbody>
</table>

### Text Books:

**Reference Books:**

**Practical Examination:**
The practical examination will be of three hours duration. It will consist of one experiment conducted during the course and an Oral examination based on the syllabus.

**EXD-475 List of Experiments:**
1. To set up an active satellite link and demonstrate link fail operation.
2. To communicate voice signal through satellite link.
3. To establish analog/digital Communication link and transmit and receive three signals (audio, video, tone) simultaneously using satellite communication trainer.
4. To transmit and receive PC data through satellite link.
5. To find the link C/N Ratio
6. Evaluation of SNR in Satellite Links
7. To observe effect of Fading margin of received signal in satellite link

**Section A: Unit 1, 2, 3**
**Section B: Unit 4, 5, 6**

**PATTERN OF QUESTION PAPER**
Six units in the syllabus shall be divided into equal parts i.e. three units in each part. Question paper shall be set having two sections A and B, as per weightage of units. Section A question shall be set on first part and section B on second part. Question paper should cover entire syllabus.

**For 80 Marks papers:**
1. Section A & Section B should be of 40 marks each.
2. Five questions in each section.
3. Out of five four questions asked should be of 15 Marks & one question asked should be 10 Marks.
4. 10 marks question will be compulsory.
Dr. BABASAHEB AMBEDKAR MARATHWADA UNIVERSITY, AURANGABAD  
FACULTY OF ENGINEERING AND TECHNOLOGY  
FINAL YEAR (EC/ECT/E&C/IE) ENGINEERING

<table>
<thead>
<tr>
<th>SEMESTER-II</th>
</tr>
</thead>
<tbody>
<tr>
<td>EXD-495 – Open Elective (EL-II For EC/ECT/E&amp;C)</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Teaching Scheme: 4Hrs/week</th>
<th>Examination Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Practical: 2Hrs/week</td>
<td>Theory Examination : 80 Marks</td>
</tr>
<tr>
<td></td>
<td>Class Test : 20 Marks</td>
</tr>
<tr>
<td></td>
<td>Practical/Oral : ---</td>
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<tr>
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<td>Term Work: ---</td>
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</tbody>
</table>

This open elective subject syllabus can be select as per the requirement of the industry and institute with proper permission of competent authority.
Dr. BABASAHEB AMBEDKAR MARATHWADA UNIVERSITY, AURANGABAD  
FACULTY OF ENGINEERING AND TECHNOLOGY  
FINAL YEAR (EC/ECT/E&C/IE) ENGINEERING  

<table>
<thead>
<tr>
<th>SEMESTER-II</th>
<th>EXD424- Project Part-II</th>
</tr>
</thead>
<tbody>
<tr>
<td>Teaching Scheme:</td>
<td>Examination Scheme</td>
</tr>
<tr>
<td>Practical: 6Hrs/week</td>
<td>Theory Examination : -</td>
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<tr>
<td></td>
<td>Class Test : -</td>
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<tr>
<td></td>
<td>Practical/Oral : 100 Marks</td>
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<td>Term Work: 50</td>
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Term –work:  
Project part II will be continuation of project part-I under taken by the candidates in the first term. The term work shall consist of a typed report of about 60 pages on the work carried out by a batch of students in respect of the project assigned during the first term part-I and the second term Part-II .  

Practical Examination:  
It shall consist of an oral examination based on the report submitted by the candidates and or the demonstration of the fabricated design project. The said examination will be conducted by a panel of two examiners consisting of preferably the guide working as a senior and other external examiner preferably from Industry or the university.  

1. Preferably project shall be useful to the general community such as rural, former community and small scale industry etc.  
2. If the project is based on software, it shall impart sufficient knowledge of software and its application to the students. The software used should not be among the software recommended in undergraduate curriculum. It should be entirely new to the students.  
3. If the project is based on Hardwar or some fabrication, it shall be supported by design and development.  
4. It is essential that the student/s should concentrate on need, feasibility, economy, usefulness, effects on environment and global warming.  
5. The student/s should get their project topic approved by the project committee under the leadership of project in charge / HOD appointed by Principal.  
6. Student has to collect information from hand book, research journals, reference books, proceeding of conference through library or internet.  
8. Student/s should prepare a spiral bound report with detail schedule of activities planned for completion of project and its presentation similar to the seminar report and shall be presented by all the partners dividing presentation among them at the time of examination in presence of guide and external examiner.  
9. Students shall prepare paper / project to participate in State level / National / International competition. The projects participated shall get additional benefit in final semester based on certificate of participation.  

Note:  
The candidate must bring the project part-I report and the final report completed in all respect while appearing for practical examination of the project.